Multilayer Ceramic Chip Capacitors

C3216X6S0G107M160AC

Product Status	Production
TDK Item Description	C3216X6S0G107MT****
Applications	Commercial Grade
Feature	General (Up to 75V)
Series	C3216 [EIA 1206]
Brand	TDK
Environmental Compliance	RoHS, REACH, Halogen Free, Lead Free



Images are for reference only and show example products.

Catalog
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Tech Info

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DOCUMENTS
- Specification
- RoHS Certificate
- SVHC / REACH Certificate
- Sample Kits

TECHNICAL SUPPORT TOOLS
- Parts Selection Tool TDK Meister
- S-parameter
- SPICE Netlist (Simple)
- SPICE Netlist (Precision)
- Equivalent Circuit Model
- DC Bias / Superimpotision Model (HSpice)
- DC Bias / Superimpotision Model (LTSpice)
- DC Bias / Superimpotision Model (PSpice)

3RD PARTY ECAD MODELS
- SnapMagic (Symbol, Footprint, 3D model)

Size

Length(L)	3.20mm +0.30,-0.10mm
Width(W)	1.60mm +0.30,-0.10mm
Thickness(T)	1.60mm +0.30,-0.10mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	1.00mm Min.
Recommended Land Pattern (PA)	2.10mm to 2.50mm(Flow Soldering) 2.00mm to 2.40mm(Reflow Soldering)
Recommended Land Pattern (PB)	1.10mm to 1.30mm(Flow Soldering) 1.00mm to 1.20mm(Reflow Soldering)
Recommended Land Pattern (PC)	1.00mm to 1.30mm(Flow Soldering) 1.10mm to 1.60mm(Reflow Soldering)

Electrical Characteristics

Capacitance	100μF ±20%
Rated Voltage	4VDC
Temperature Characteristic	X6S(±22%)
Dissipation Factor (Max.)	10%
Insulation Resistance (Min.)	1MΩ

Other

Operating Temp. Range	-55 to 105°C
Soldering Method	Wave (Flow) Reflow
AEC-Q200	NO
Packing	Embossed (Plastic)Taping [180mm Reel]
Package Quantity	2000pcs
FIT (Failure In Time)	This will be displayed when you login to myTDK

Related articles



Replacing Electrolytic Capacitor with MLCC, Revised Guide

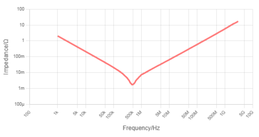
Traditionally, aluminum electrolytic capacitors and tantalum electrolytic capacitors

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Characteristic Graph

This data is for reference only and does not guarantee product characteristics.

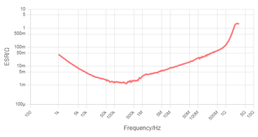
Impedance



C3216X6S0G107M160AC

GRAPH SETTINGS

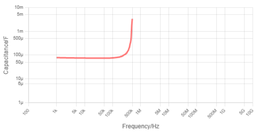
ESR



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GRAPH SETTINGS

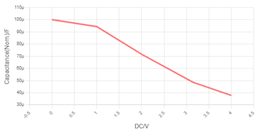
Capacitance



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GRAPH SETTINGS

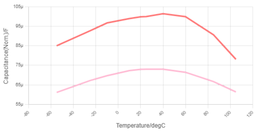
DC Bias Characteristic



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GRAPH SETTINGS

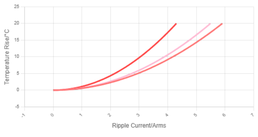
Temperature Characteristic



C3216X6S0G107M160AC(No Bias)
C3216X6S0G107M160AC(DC Bias = 2V)

GRAPH SETTINGS

Ripple Temperature Rising



C3216X6S0G107M160AC(100kHz)
C3216X6S0G107M160AC(500kHz)
C3216X6S0G107M160AC(1MHz)

GRAPH SETTINGS

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MULTILAYER CERAMIC CHIP CAPACITORS
C1005X5R1E474M050BB
Capacitance=0.47μF
Edc=25V
T.C.=X5R
LxWxT:1x0.5x0.5mm
General



MULTILAYER CERAMIC CHIP CAPACITORS
C1005X7S0G225K050BC
Capacitance=2.2μF
Edc=4V
T.C.=X7S
LxWxT:1x0.5x0.5mm
General



MULTILAYER CERAMIC CHIP CAPACITORS
C2012X6S0G476M125AC
Capacitance=47μF
Edc=4V
T.C.=X6S
LxWxT:2x1.25x1.25mm
General

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