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SSD1881

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Advance Information

MLA COLUMN (SEGMENT) DRIVER

CMOS

The SSD1881 is an MLA (Multi Line Addressing), 160 outputs and triple-value low resistance common driver. Joining with the SSD1730 MLA power chip and the SSD1870 column driver, an MLALCD module system having high picture quality, high speed responses and low power consumption can be produced. SSD1881 can be configured as 140 outputs or 160 outputs.

[▶ Features](#)

- Power supply to logic system, 2.7V - 5.5V
- LCD drive voltage, 14V - 30V
- 140 or 160 common outputs
- Drive duty, 1/160 and 1/320
- 1/2P and 1P operation mode
- Adjustable LCD power source offset bias
- Non-biased display off function
- Selectable output shift direction
- Cascade supported
- Available in Gold-bumped die and TAB (Tape Automated Bonding) Package

[▶ Ordering Information](#)

Ordering Part No.	Package	Pad Pitch/Outer Lead Pitch
SSD1881Z	Gold Bump Die	N/A
SSD1881TR1	TAB	0.2295mm
SSD1881T1R1	TAB	0.14mm
SSD1881T2R1	TAB	0.22mm

Contact our sales representative for more information:

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or

[Request a datasheet](#)