

# Dual Switching Diode Common Cathode

## BAV70L, SBAV70L

### Features

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

### MAXIMUM RATINGS (EACH DIODE)

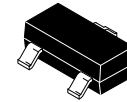
| Rating                                                                                        | Symbol    | Value | Unit |
|-----------------------------------------------------------------------------------------------|-----------|-------|------|
| Reverse Voltage                                                                               | $V_R$     | 100   | V    |
| Forward Current                                                                               | $I_F$     | 200   | mA   |
| Repetitive Peak Forward Current<br>(Pulse Wave = 1 sec, Duty Cycle = 66%)                     | $I_{FRM}$ | 1.5   | A    |
| Non-Repetitive Peak Forward Current<br>(Square Wave, $T_J = 25^\circ\text{C}$ prior to surge) | $I_{FSM}$ |       | A    |
| $t = 1\ \mu\text{s}$                                                                          |           | 31    |      |
| $t = 10\ \mu\text{s}$                                                                         |           | 16    |      |
| $t = 100\ \mu\text{s}$                                                                        |           | 10    |      |
| $t = 1\ \text{ms}$                                                                            |           | 4.5   |      |
| $t = 10\ \text{ms}$                                                                           |           | 2.5   |      |
| $t = 100\ \text{ms}$                                                                          |           | 1.0   |      |
| $t = 1\ \text{s}$                                                                             |           | 0.5   |      |

### THERMAL CHARACTERISTICS

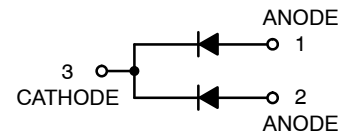
| Characteristic                                                                                                         | Symbol          | Max         | Unit                      |
|------------------------------------------------------------------------------------------------------------------------|-----------------|-------------|---------------------------|
| Total Device Dissipation FR-5 Board<br>(Note 1)<br>$T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$         | $P_D$           | 225         | mW                        |
| Thermal Resistance,<br>Junction-to-Ambient                                                                             | $R_{\theta JA}$ | 556         | $^\circ\text{C}/\text{W}$ |
| Total Device Dissipation<br>Alumina Substrate,<br>(Note 2) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$ | $P_D$           | 300         | mW                        |
| Thermal Resistance,<br>Junction-to-Ambient                                                                             | $R_{\theta JA}$ | 417         | $^\circ\text{C}/\text{W}$ |
| Junction and Storage Temperature Range                                                                                 | $T_J, T_{stg}$  | -55 to +150 | $^\circ\text{C}$          |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

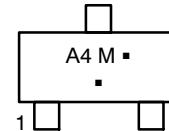
1. FR-5 =  $1.0 \times 0.75 \times 0.062$  in.
2. Alumina =  $0.4 \times 0.3 \times 0.024$  in. 99.5% alumina.



SOT-23 (TO-236)  
CASE 318  
STYLE 9



### MARKING DIAGRAM



A4 = Device Code  
M = Date Code\*  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\* Date Code orientation and/or overbar may vary depending upon manufacturing location.

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

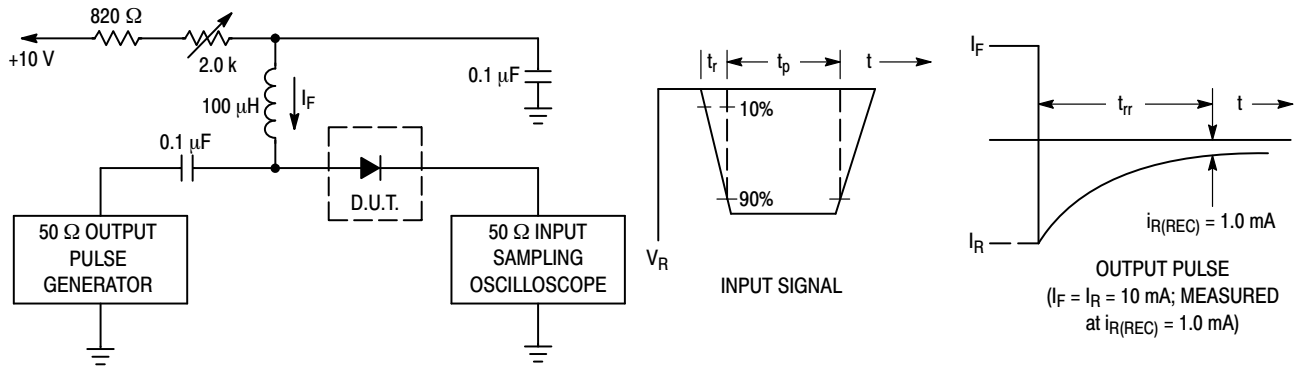
# BAV70L, SBAV70L

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted) (Each Diode)

| Characteristic                                                                                                                                                                        | Symbol     | Min | Max                        | Unit          |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------|-----|----------------------------|---------------|
| Reverse Breakdown Voltage<br>( $I_{(BR)} = 100\ \mu\text{A}$ )                                                                                                                        | $V_{(BR)}$ | 100 |                            | V             |
| Reverse Voltage Leakage Current (Note 3)<br>( $V_R = 25\ \text{V}$ , $T_J = 150^\circ\text{C}$ )<br>( $V_R = 100\ \text{V}$ )<br>( $V_R = 70\ \text{V}$ , $T_J = 150^\circ\text{C}$ ) | $I_R$      |     | 60<br>1.0<br>100           | $\mu\text{A}$ |
| Diode Capacitance<br>( $V_R = 0\ \text{V}$ , $f = 1.0\ \text{MHz}$ )                                                                                                                  | $C_D$      |     | 1.5                        | pF            |
| Forward Voltage<br>( $I_F = 1.0\ \text{mA}$ )<br>( $I_F = 10\ \text{mA}$ )<br>( $I_F = 50\ \text{mA}$ )<br>( $I_F = 150\ \text{mA}$ )                                                 | $V_F$      |     | 715<br>855<br>1000<br>1250 | mV            |
| Reverse Recovery Time<br>( $I_F = I_R = 10\ \text{mA}$ , $I_{R(REC)} = 1.0\ \text{mA}$ ) (Figure 1)                                                                                   | $t_{rr}$   |     | 6.0                        | ns            |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. For each individual diode while second diode is unbiased.



- Notes: 1. A 2.0 kΩ variable resistor adjusted for a Forward Current ( $I_F$ ) of 10 mA.  
2. Input pulse is adjusted so  $I_{R(\text{peak})}$  is equal to 10 mA.  
3.  $t_p \gg t_{rr}$

**Figure 1. Recovery Time Equivalent Test Circuit**

# BAV70L, SBAV70L

## Curves Applicable to Each Anode

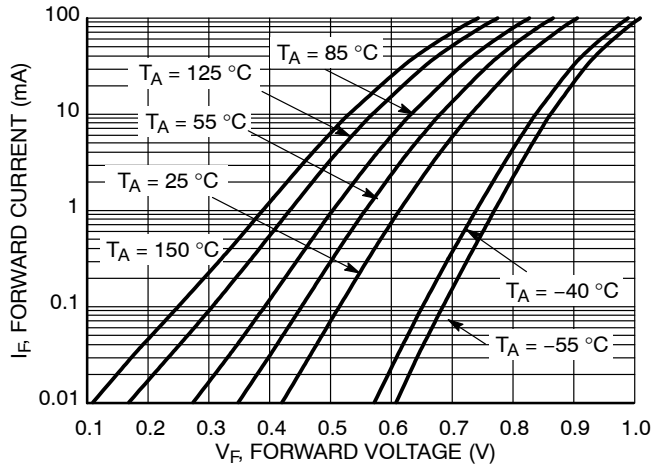


Figure 2. Forward Voltage

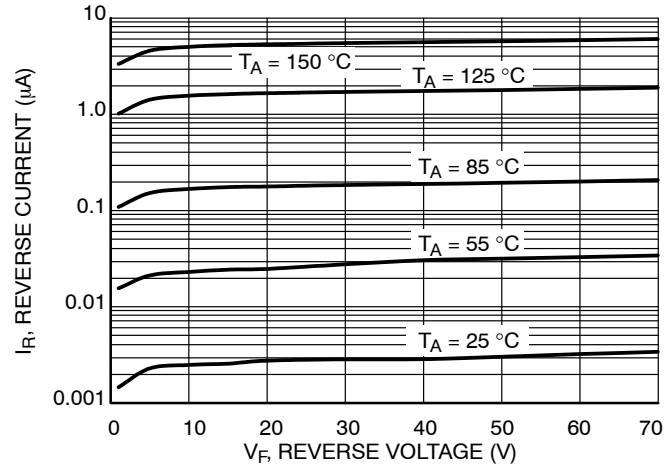


Figure 3. Leakage Current

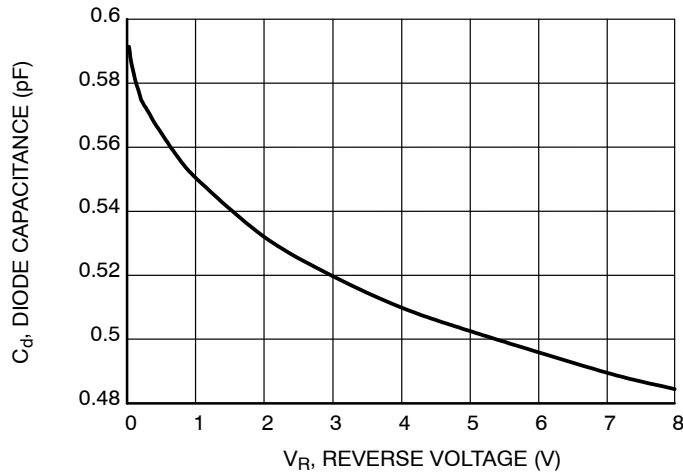


Figure 4. Capacitance

## ORDERING INFORMATION

| Device                                                   | Specific Marking Code | Package             | Shipping†            |
|----------------------------------------------------------|-----------------------|---------------------|----------------------|
| BAV70LT1G<br>BAV70LT1G-NC<br>SBAV70LT1G<br>SBAV70LT1G-NC | A4                    | SOT-23<br>(Pb-Free) | 3000 / Tape & Reel   |
| BAV70LT3G<br>BAV70LT3G-NC<br>SBAV70LT3G<br>SBAV70LT3G-NC |                       |                     | 10,000 / Tape & Reel |

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, [BRD8011/D](#).

\* S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

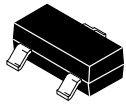
## BAV70L, SBAV70L

### REVISION HISTORY

| Revision | Description of Changes                    | Date     |
|----------|-------------------------------------------|----------|
| 15       | Changes to the ordering information table | 9/4/2026 |

This document has undergone updates prior to the inclusion of this revision history table. The changes tracked here only reflect updates made on the noted approval dates.

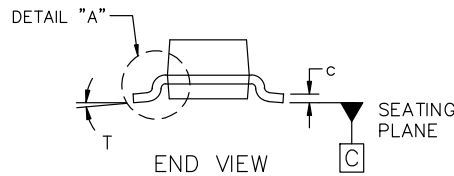
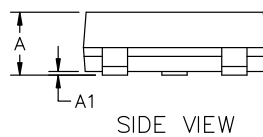
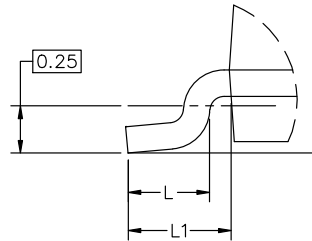
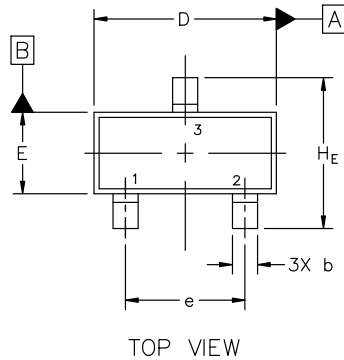




SCALE 4:1

**SOT-23 (TO-236) 2.90x1.30x1.00 1.90P**  
CASE 318  
ISSUE AU

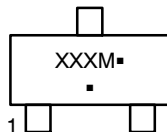
DATE 14 AUG 2024



| MILLIMETERS |      |      |      |
|-------------|------|------|------|
| DIM         | MIN  | NOM  | MAX  |
| A           | 0.89 | 1.00 | 1.11 |
| A1          | 0.01 | 0.06 | 0.10 |
| b           | 0.37 | 0.44 | 0.50 |
| c           | 0.08 | 0.14 | 0.20 |
| D           | 2.80 | 2.90 | 3.04 |
| E           | 1.20 | 1.30 | 1.40 |
| e           | 1.78 | 1.90 | 2.04 |
| L           | 0.30 | 0.43 | 0.55 |
| L1          | 0.35 | 0.54 | 0.69 |
| HE          | 2.10 | 2.40 | 2.64 |
| T           | 0°   | ---  | 10°  |

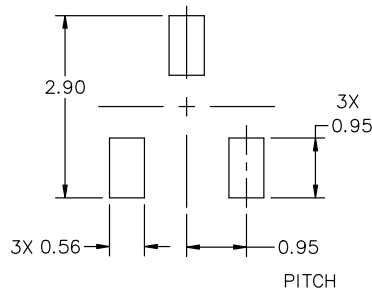
## NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
2. CONTROLLING DIMENSIONS: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

**GENERIC MARKING DIAGRAM\***


XXX = Specific Device Code  
M = Date Code  
▪ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.


**RECOMMENDED MOUNTING FOOTPRINT**

\* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

**STYLES ON PAGE 2**

|                         |                                             |                                                                                                                                                                                  |
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**CASE 318**  
**ISSUE AU**

DATE 14 AUG 2024

|                                                         |                                                       |                                                             |                                                             |                                                             |                                                             |
|---------------------------------------------------------|-------------------------------------------------------|-------------------------------------------------------------|-------------------------------------------------------------|-------------------------------------------------------------|-------------------------------------------------------------|
| STYLE 1 THRU 5:<br>CANCELLED                            | STYLE 6:<br>PIN 1. BASE<br>2. EMITTER<br>3. COLLECTOR | STYLE 7:<br>PIN 1. EMITTER<br>2. BASE<br>3. COLLECTOR       | STYLE 8:<br>PIN 1. ANODE<br>2. NO CONNECTION<br>3. CATHODE  |                                                             |                                                             |
| STYLE 9:<br>PIN 1. ANODE<br>2. ANODE<br>3. CATHODE      | STYLE 10:<br>PIN 1. DRAIN<br>2. SOURCE<br>3. GATE     | STYLE 11:<br>PIN 1. ANODE<br>2. CATHODE<br>3. CATHODE-ANODE | STYLE 12:<br>PIN 1. CATHODE<br>2. CATHODE<br>3. ANODE       | STYLE 13:<br>PIN 1. SOURCE<br>2. DRAIN<br>3. GATE           | STYLE 14:<br>PIN 1. CATHODE<br>2. GATE<br>3. ANODE          |
| STYLE 15:<br>PIN 1. GATE<br>2. CATHODE<br>3. ANODE      | STYLE 16:<br>PIN 1. ANODE<br>2. CATHODE<br>3. CATHODE | STYLE 17:<br>PIN 1. NO CONNECTION<br>2. ANODE<br>3. CATHODE | STYLE 18:<br>PIN 1. NO CONNECTION<br>2. CATHODE<br>3. ANODE | STYLE 19:<br>PIN 1. CATHODE<br>2. ANODE<br>3. CATHODE-ANODE | STYLE 20:<br>PIN 1. CATHODE<br>2. ANODE<br>3. GATE          |
| STYLE 21:<br>PIN 1. GATE<br>2. SOURCE<br>3. DRAIN       | STYLE 22:<br>PIN 1. RETURN<br>2. OUTPUT<br>3. INPUT   | STYLE 23:<br>PIN 1. ANODE<br>2. ANODE<br>3. CATHODE         | STYLE 24:<br>PIN 1. GATE<br>2. DRAIN<br>3. SOURCE           | STYLE 25:<br>PIN 1. ANODE<br>2. CATHODE<br>3. GATE          | STYLE 26:<br>PIN 1. CATHODE<br>2. ANODE<br>3. NO CONNECTION |
| STYLE 27:<br>PIN 1. CATHODE<br>2. CATHODE<br>3. CATHODE | STYLE 28:<br>PIN 1. ANODE<br>2. ANODE<br>3. ANODE     |                                                             |                                                             |                                                             |                                                             |

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