

16-Bit 1.2-2.5V to 3.3V Level Shifting Transceiver with 3-State Outputs

Features

- → PI74AVC164245 is designed for low voltage operation: 1.2V 2.5V to 3.3V
- → Industrial operation at -40°C to +85°C
- → Packaging (Pb-free & Green):
 - 48-pin plastic 300-mil SSOP (V)
 - 48-pin plastic 240-mil TSSOP (A)
 - 48-pin plastic 173-mil TVSOP (K)

Truth Table (each 8-bit section)

Inputs	Operation
OE DIR	-
L L L H	B data to A bus A data to B bus
H X	Isolation

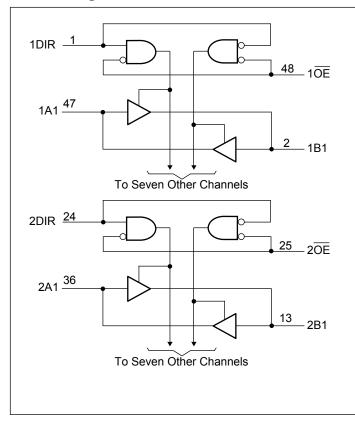
Description

Pericom Semiconductor's PI74AVC164245, a 16-bit (dual-octal) noninverting bus transceiver, contains two separate supply rails: B port (V_{CCB}), set at 3.3V, and A port (V_{CCA}), set to operate at 1.2-2.5V. This arrangement permits translation from a 1.2-2.5V to 3.3V environment and vice versa.

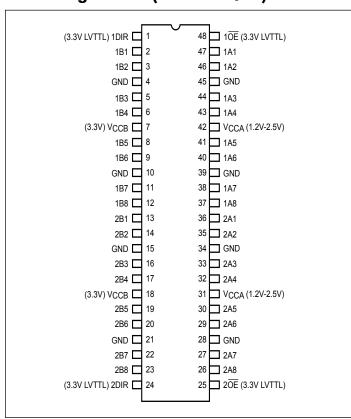
The PI74AVC164245 is designed for asynchronous communication between data buses.

To ensure the high impedance state during power up or power down, the output-enable (\overline{OE}) input should be tied to V_{CCB} through a pullup resistor: the minimum value of the resistor is determined by the current-sinking capability of the driver.

Block Diagram



Pin Configuration (28-Pin TQFN)



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Maximum Ratings

(Absolute maximum ratings over operating free-air temperature range from V_{CCB} at 3.3V & V_{CCA} at 1.5 - 2.5V, unless otherwise **Note:**

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Supply Voltage Range V_{CCA} 0.5V to 3.8V V_{CCB} 0.5V to 4.6V	Continuous output current, I $_{\rm O}$ $\pm 50 {\rm mA}$ Continuous current through each V $_{\rm CC}$ or GND $\pm 100 {\rm mA}$
Input Voltage Range, V_I Except I/O ports ⁽¹⁾ . $-0.5V$ to $4.6V$ I/O port $A^{(2)}$ $-0.5V$ to V_{CCA} +0.5V I/O port $B^{(1)}$ $-0.5V$ to V_{CCB} +0.5V	Package thermal impedance, 0 _{JA} (3): A package
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Recommended Operating Conditions for V_{CCB} at 3.3 $V^{(4)}$

		Min.	Max.	Units
V _{CCB}	Supply voltage	3.0	3.6	
$V_{_{ m IH}}$	High-level voltage	2		
V _{IL}	Low-level voltage		0.8	V
$V_{_{\mathrm{IB}}}$	Input Voltage	0	V_{CCB}	
V_{OB}	Output Voltage	0	$V_{_{\mathrm{CCB}}}$	
I_{OH}	High-level output current		-12	m A
I_{OL}	Low-level output current		12	mA
$\Delta t/\Delta v$	Input transition rise or fall rate		10	ns/V
T_{A}	Operating free-air temperature	-40	85	°C



Recommended Operating Conditions for V_{cca} at 2.5 $V^{(4)}$

			Min.	Max.	Units
V _{CCA}	Supply voltage		2.3	2.7	
V _{IH}	High-level voltage	$V_{CCA} = 2.3V \text{ to } 2.7V$	1.7		
V _{IL}	Low-level voltage	$V_{CCA} = 2.3V \text{ to } 2.7V$		0.7	V
V _{IA}	Input Voltage		0	V _{CCA}	
Voa	Output Voltage		0	V _{CCA}	
т	III:-l. lltt	$V_{CCA} = 2.3V$		-8	
1 _{OH}	High-level output current	$V_{CCA} = 2.7V$		-12	A
т	T 11	$V_{CCA} = 2.3V$		8	mA
I_{OL}	Low-level output current	$V_{CCA} = 2.7V$		12	
$\Delta t/\Delta v$	Input transition rise or fall rate			10	ns/V
T_{A}	Operating free-air temperature		-40	85	°C

Notes:

- 1. This value is limited to 4.6V maximum.
- 2. This value is limited to 3.8V maximum.
- 3. The package thermal impedance is calculated in accordance with JESD 51.
- 4. To ensure proper device operation, all unused device inputs must be held at the associated V_{CC} or GND.

Recommended Operating Conditions for $V_{\rm ccA}$ at 1.8 $V^{(4)}$

			Min.	Max.	Units
V _{CCA}	Supply voltage		1.65	1.95	
$V_{_{\mathrm{IH}}}$	High-level voltage	$V_{CCA} = 1.65V$ to 1.95V	0.65 x V _{CCA}		
V _{IL}	Low-level voltage	$V_{CCA} = 1.65V \text{ to } 1.95V$		0.35 x V _{CCA}	V
V_{IB}	Input Voltage		0	V _{CCA}	
V _{OA}	Output Voltage		0	V _{CCA}	
T	High lovel output output	$V_{CCA} = 1.65V$		-4	
I _{OH}	High-level output current	$V_{CCA} = 1.95V$		-8	A
т	Love lovel output assument	$V_{CCA} = 1.65V$		4	mA
I _{OL}	Low-level output current	$V_{CCA} = 1.95V$		8	
Δt/Δv	Input transition rise or fall rate			10	ns/V
T_{A}	Operating free-air temperature		-40	85	°C

Notes:

1. To ensure proper device operation, all unused device inputs must be held at the associated VCC or GND.



Electrical Characteristics (V_{CCB}) (Over recommended operating free-air temperature range for $V_{CCB} = 3.3V$)

Parameter		Test Conditions	\mathbf{V}_{CCB}	Min.	Typ.†	Max.	Units
		$I_{OH} = -100 \mu A$	3.0V	2.8			
V _{OH} (A to B)		10H=-100μΑ	3.6V	3.4			
		T 12 A	3.0V	2.2			
		$I_{OH} = -12 \text{mA}$	3.6V	2.8			V
		T 100 A	3.0V			0.2	•
17 (A (D)		$I_{OL} = -100 \mu A$	3.6V			0.2	
V _{OL} (A to B)		I _{OL} = -12mA	3.0V			0.5	
			3.6V			0.5	
$I_{\rm I}$	Control Inputs	$V_{\rm I} = V_{\rm CCB}$ or GND	3.6V			±5	
I_{oz}^{\ddagger}	A or B ports	$V_O = V_{CCB}$ or GND	3.6V			±10	
I_{CC}		$V_I = V_{CCB}$ or GND, $I_O = 0$	3.6V			20	μΑ
$\Delta I_{_{ m CC}}{}^*$		One input at V_{CCB} –0.6V, Other inputs at V_{CCB} or GND	3.0V to 3.6V			500	
C _I	Control Inputs	$V_{I} = V_{CCB}$ or GND	3.3V		4.5		
C _{IO}	A or B ports	$V_{O} = V_{CCB}$ or GND	3.3V		6.5		pF

[†] Typical values are measured at $V_{CCB} = 3.3V$, $T_A = 25$ °C

 $[\]ddagger$ For I/O ports, the parameter I_{OZ} includes the input leakage current.

^{*} This is the increase in supply current for each input that is at one of the specified TTL voltage levels rather than at 0 or the associated $V_{\rm CC}$.



Electrical Characteristics (V_{CCA}) (Over recommended operating free-air temperature range for $V_{CCA} = 2.5V$)

Parameter		Test Conditions	V _{CCA}	Min.	Typ.†	Max.	Units
Very (P to A)		I 100A	2.3V to 2.7V	V _{CC} - 0.1			
		$I_{OH} = -100 \mu A$	2.3V	1.7			
V _{OH} (B to A)		$I_{OH} = -8mA$	2.3V	2.0			
		$I_{OH} = -12mA$	2.7V	1.8			V
		$I_{OL} = -100 \mu A$	2.3V to 2.7V			0.2	
V _{OL} (B to A)		$I_{OL} = -8mA$	2.3V			0.4	
		$I_{OL} = -12 \text{mA}$	2.7V			0.5	
I_{I}	Control Inputs	$V_{\rm I} = V_{\rm CCA}$ or GND	2.7V			±5	
I _{oz} ‡	A or B ports	$V_{O} = V_{CCB}$ or GND	2.7V			±10	
I_{CC}		$V_{\rm I} = V_{\rm CCA}$ or GND, $I_{\rm O} = 0$	2.7V			30	μΑ
ΔI_{cc}^*		One input at V_{CCA} –0.6V, Other inputs at V_{CCA} or GND	2.3V to 2.7V			500	
C _I	Control Inputs	V _I = V _{CCA} or GND	2.5V		4.5		nE
C _{IO}	A or B ports	$V_{O} = V_{CCA}$ or GND	2.5V		6.5		pF

[†] Typical values are measured at $V_{CCA} = 2.5V$, $T_A = 25$ °C

^{*} This is the increase in supply current for each input that is at one of the specified TTL voltage levels rather than at 0 or the associated V_{CC}.



Electrical Characteristics (V_{CCA}) (Over recommended operating free-air temperature range for $V_{CCA} = 1.8V$)

Parameter	:	Test Conditions	\mathbf{V}_{CCA}	Min.	Typ.**	Max.	Units	
		$I_{OH} = -100 \mu A$	1.65V to 1.95V	V _{CC} - 0.1				
V _{OH} (B to A)	I 4 A	1.65V	1.3				
	•	$I_{OH} = -4mA$	1.95V	1.4				
		$I_{OH} = -8mA$	1.95V	1.3			V	
		$I_{OL} = 100 \mu A$	1.65V			0.2		
V _{OL} (B to A))	$I_{OL} = 4mA$	1.95V			0.3		
		$I_{OL} = 8mA$	1.95V			0.4		
I_OZ ***	A or B ports	$V_{O} = V_{CCA}$ or GND	1.95V			±5		
I_{CC}		$V_I = V_{CCA}$ or GND, $I_O = 0$	1.95V			±20] .	
ΔI_{cc}^*		One input at V_{CCA} – 6V, Other inputs at V_{CCA} or GND	1.65V to 1.95V			300	μΑ	
C _I	Control Inputs	V _I = V _{CCA} or GND	1.8V		4.5		, DE	
C _{IO}	A or B ports	$V_{O} = V_{CCA}$ or GND	1.8V		6.5		pF	

^{*} The increase in supply current for each input at one of the specified TTL voltage levels rather than at the associated $V_{\rm CC}$.

^{**} Typical values are measured at $V_{CCA} = 1.8V$, $T_A = 25$ °C

^{***} For I/O ports, the parameter $I_{\rm OZ}$ includes the input leakage current.



Recommended Operating Conditions for V_{cca} at 1.2V

			Min.	Typ.	Max.	Units
V _{CCA}	Supply voltage			1.2V		
V _{IH}	High-level voltage	$V_{CCA} = 1.2V$	0.65 x V _{CCA}			
$V_{_{ m IL}}$	Low-level voltage	$V_{CCA} = 1.2V$			0.35 x V _{CCA}	V
V_{IB}	Input Voltage		0		V _{CCA}	
VOA	Output Voltage		0		V _{CCA}	
ОН	High-level output current	$V_{CCA} = 1.2V$		-2		4
OL	Low-level output current	$V_{CCA} = 1.2V$		2		mA
Δt/Δv	Input transition rise or fall rate				10	ns/V
$\Gamma_{_{ m A}}$	Operating free-air temperature		-40		85	°C

Notes:

Electrical Characteristics (V_{CCA}) (Over recommended operating free-air temperature range for $V_{CCA} = 1.2V$)

Parameter		Test Conditions	V _{CCA}	Min.	Тур.	Max.	Units
V _{OH} (B to A)		$I_{OH} = -2mA$	1.2V		1.1V		
		$I_{OL} = 100 \mu A$	1.2V		0.1		
V _{OL} (B to A)		$I_{OL} = 2mA$	1.2V		0.1		
I _{OZ} **	A or B ports	$V_O = V_{CCA}$ or GND	1.2V		±5		μΑ
I _{CC}	,	$V_I = V_{CCA}$ or GND, $I_O = 0$	1.2V		10		, ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
C _I	Control Inputs	$V_{\rm I} = V_{\rm CCA}$ or GND	1.2V		4.5		
C _{IO}	A or B ports	V _O = V _{CCA} or GND	1.2V		6.5		pF

^{1.} To ensure proper device operation, all unused device inputs must be held at the associated V_{cc} or GND.

^{*} The increase in supply current for each input at one of the specified TTL voltage levels rather than at the associated V_{CC}.

^{**} For I/O ports, the parameter $I_{\rm OZ}$ includes the input leakage current.



Switching Characteristics (C_L = see Figures 1,2,3,4)

				PI74AVC164245			PI74AVC164245A					
				$V_{CCB} = 3.3$	3V ±0.3V		V	$V_{\rm CCB} = 3$.3V ±0.3	V		
	From	То	V _{CCA} = ±0.1			= 2.5V .2V	$V_{CCA} = 1.2V$		= 1.8V .15V		= 2.5V .2V	Units
Parameter	(Input)	(Output)	Min.	Max.	Min.	Max.	Тур.	Min.	Max.	Min.	Max.	
+	A	В	1.5	4.6	1.5	4.2	3.5	1.5	3.5	1.0	3.0	
$t_{\scriptscriptstyle{\mathrm{PD}}}$	В	A	1.5	4.4	1.5	4.0	3.0	1.5	3.5	1.0	2.8	
$t_{\scriptscriptstyle\mathrm{EN}}$	ŌĒ	В	1.7	5.5	1.7	4.5	2.8	1.7	4.5	1.2	3.5	
t _{DIS}	ŌĒ	В	1.5	5.5	1.5	4.5	2.2	1.5	4.5	1.0	3.5	ns
t_{EN}	ŌĒ	A	1.7	5.5	1.7	4.5	4.2	1.7	4.5	1.2	3.5	113
t _{DIS}	ŌĒ	A	2.0	5.5	2.0	4.5	3.3	2.0	4.5	1.5	3.5	
t _{SK(O)} ⁽¹⁾						0.5					0.3	
t _{SK(b)} (1)						0.3					0.25	

Notes:

Operating Conditions for $T_A = 25$ °C

ı			$V_{CCA} = 2.5V$ $V_{CCB} = 3.3V$	
Parameters	Test Conditions	Typical	Units	
C. Dowar Dissinction Conscitance	Outputs Enabled (A or B)	$C_L = 30pF$	56	nE
C _{PD} Power Dissipation Capacitance	Outputs Disabled	F = 10MHz	6	pF

^{1.} This is the skew between any two outputs of the same package, and switching in the same direction. For $t_{SK(O)}$, Output 1 and Output 2 are any two outputs. For $t_{SK(D)}$, Output 1 and Output 2 are on the same bank.



Parameter Measurement Information, $V_{cco} = 1.2V$

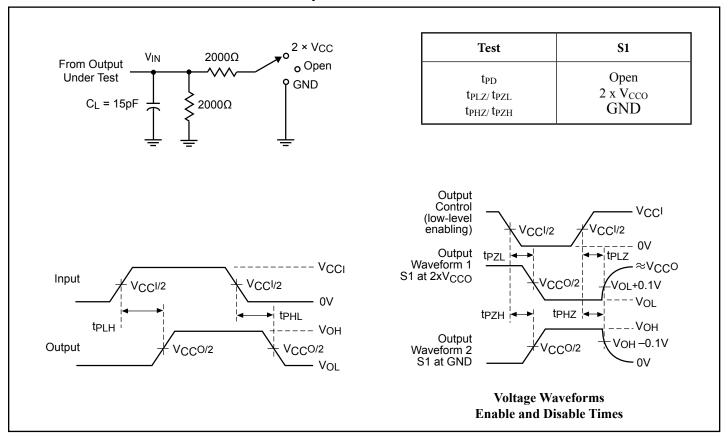


Figure 1. Load Circuit and Voltage Waveforms

Notes:

C₁ includes probe and jig capacitance.

Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control. All input pulses are supplied by generators having the following characteristics: $PRR \le 10Mz$, $Z_0 = 50\Omega$, $t_R \le 2.5$ ns, $t_F \le 2.5$ ns. The outputs are measured one at a time with one transition per measurement.

 t_{PLZ} and t_{PHZ} are the same as t_{DIS}

 t_{PZL} and t_{PZH} are the same as t_{EN} .



Parameter Measurement Information, $V_{cco} = 1.8V \pm 0.15V$

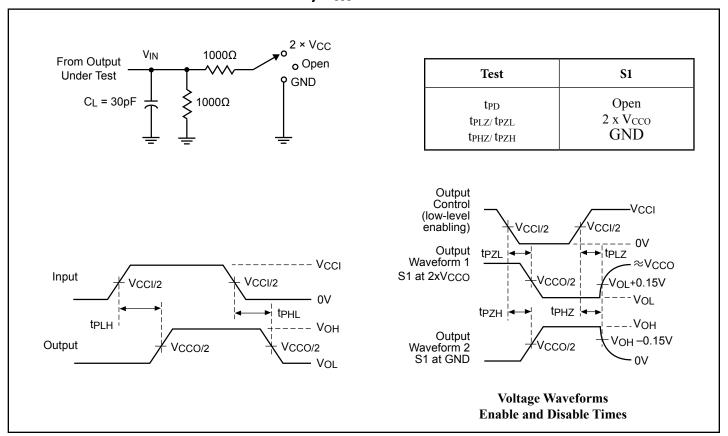


Figure 2. Load Circuit and Voltage Waveforms

Notes:

C₁ includes probe and jig capacitance.

Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control. All input pulses are supplied by generators having the following characteristics: PRR \leq 10Mz, $Z_0 = 50\Omega$, $t_R \leq$ 2.5ns, $t_F \leq$ 2.5ns. The outputs are measured one at a time with one transition per measurement.

 $t_{\rm PLZ}$ and $t_{\rm PHZ}$ are the same as $t_{\rm DIS}$. $t_{\rm PZL}$ and $t_{\rm PZH}$ are the same as $t_{\rm EN}$.



Parameter Measurement Information, $V_{cco} = 2.5V \pm 0.2V$

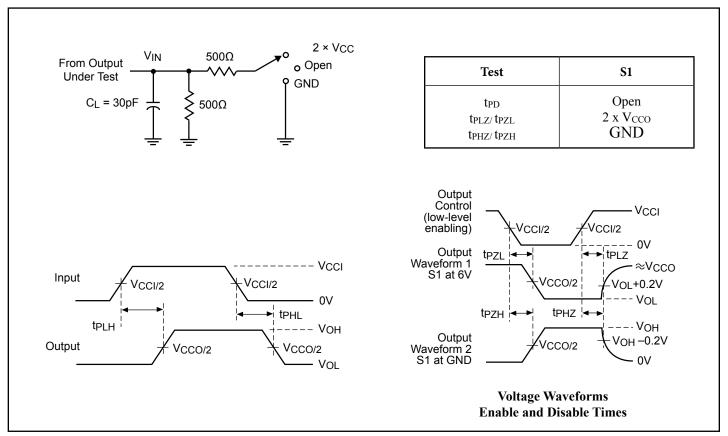


Figure 3. Load Circuit and Voltage Waveforms

Notes:

C₁ includes probe and jig capacitance.

Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control. All input pulses are supplied by generators having the following characteristics: $PRR \le 10Mz$, $Z_0 = 50\Omega$, $t_R \le 2.5$ ns, $t_F \le 2.5$ ns. The outputs are measured one at a time with one transition per measurement.

 t_{PLZ} and t_{PHZ} are the same as t_{DIS}

 t_{PZL} and t_{PZH} are the same as t_{EN} .



Parameter Measurement Information, $V_{cco} = 3.3V \pm 0.3V$

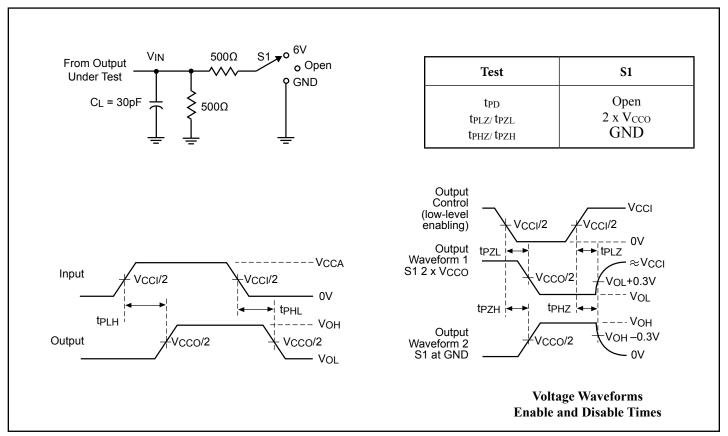


Figure 4. Load Circuit and Voltage Waveforms

Notes:

C₁ includes probe and jig capacitance.

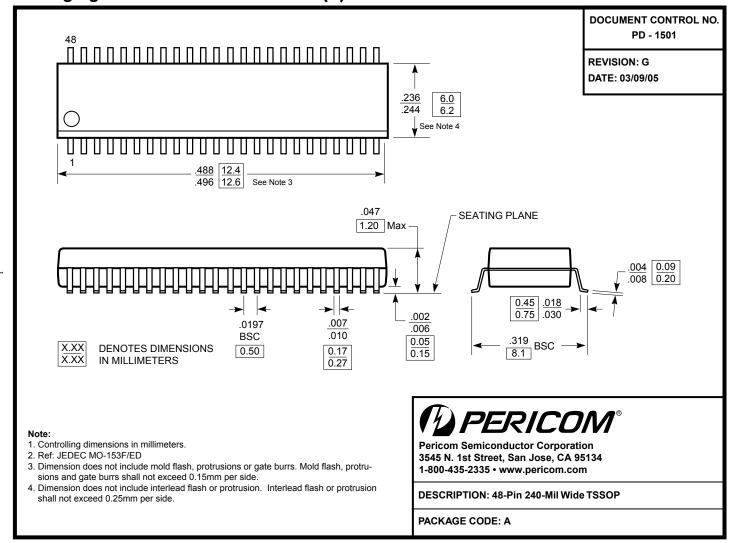
Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control. All input pulses are supplied by generators having the following characteristics: $PRR \le 10Mz$, $Z_0 = 50\Omega$, $t_R \le 2.5$ ns, $t_F \le 2.5$ ns. The outputs are measured one at a time with one transition per measurement.

 t_{PLZ} and t_{PHZ} are the same as t_{DIS}

 t_{PZL} and t_{PZH} are the same as t_{EN} .

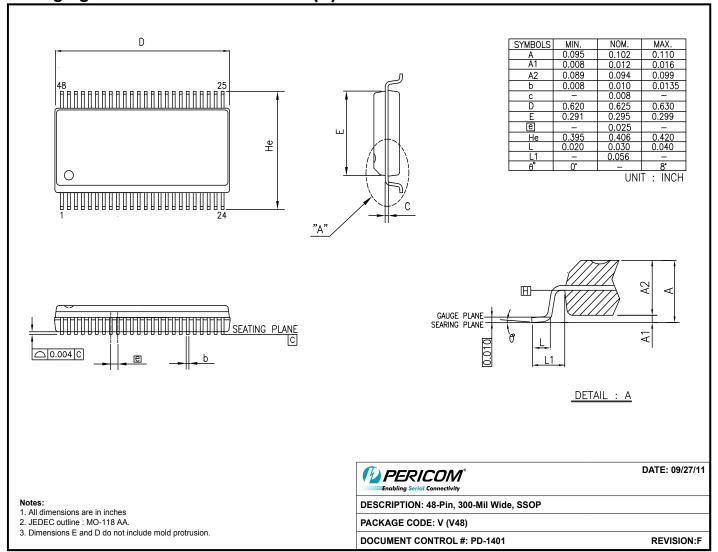


Packaging Mechanical: 48-Pin TSSOP (A)



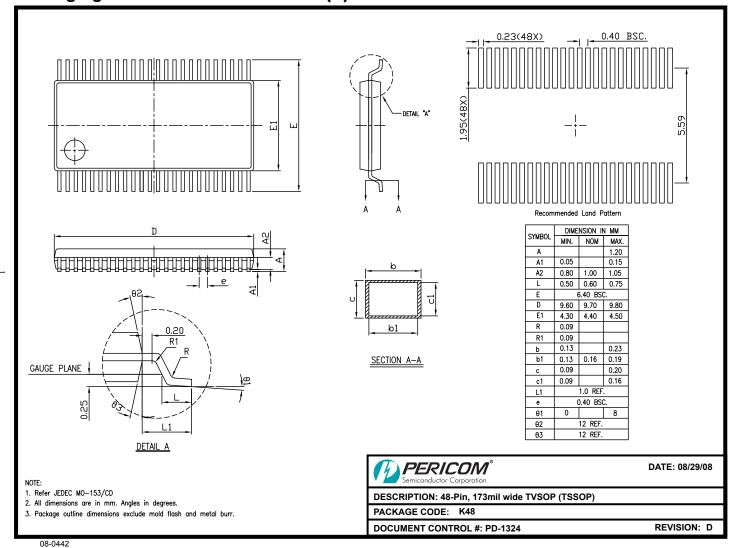


Packaging Mechanical: 48-Pin TSSOP (V)





Packaging Mechanical: 48-Pin TSSOP (K)



Ordering Information

Ordering Code	Package Code	Speed Grade	Package Description
PI74AVC164245AE	A	Blank	Pb-free & Green, 48-pin 240-mil wide plastic TSSOP
PI74AVC164245KE	K	Blank	Pb-free & Green, 48-pin 173-mil wide plastic TVSOP
PI74AVC164245AAE	A	A	Pb-free & Green, 48-pin 240-mil wide plastic TSSOP
PI74AVC164245AVE	V	A	Pb-free & Green, 48-pin 300-mil wide plastic SSOP
PI74AVC164245AKE	K	A	Pb-free & Green, 48-pin 173-mil wide plastic TVSOP

Notes:

- 1. Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- 2. E = Pb-free and Green
- 3. X Suffix =Tape/Reel

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