

QTS532

Temperature Compensated Crystal Oscillator

Features

Frequency range : 10MHz to 52MHzSMD seam sealing ceramic package

• Supply voltage: 3.3V

 Free Run Stability for 20 Years : ±4.6ppm (Stratum 3)

Clipped Sinewave output

Voltage control function available

• Tri-state function available

External dimensions (mm)
L: 5.0 x W: 3.2 x H: 2.1
RoHS compliant & Pb free

Applications

- Wireless communications, Smallcell, Base station
- Precise timing & synchronization network (IEEE1588, Sync.E), OTN, PTN, Switch
- Test and measurement equipment
- Smart grid
- Stratum 3

Electrical Characteristics

	Item	QTS532	Conditions	
Nominal Frequency Range (F ₀)		10MHz ~ 52MHz		
Frequency Tolerance (F _{tol})		±2.0 ppm Max.	Note [1]	
	vs Temperature	±0.1ppm, ±0.28ppm, ±0.5ppm	Note [2]	
Frequency Stability	vs Load	±0.1 ppm Max.	Load ±5%	
	vs Supply Voltage	±0.1 ppm Max.	V _{DD} ±5%	
Operating Tempera	ature Range (T _{OTR})	-40°C ~ +85°C		
Supply Voltage (V _{DD})		3.3V		
Current Consumption (I _{DD})		5.0 mA Max.		
Output Type		Clipped Sinewave		
Output Load		10kΩ // 10pF		
Output Level		0.8 V(p-p) Min.	Note [3]	
I I ri-state Innut -	ligh (output enable)	80% V _{DD} Min.		
· L	ow (output disable)	20% V _{DD} Max. 2.5 ms Max.		
Start-up Time Auto Frequency Control (AFC) Range		±5ppm ~ ±16ppm	VCTCXO, optional $V_C = 1.5\pm1.0V$	
Phase Noise		-135 dBc/Hz Typ.	at 1kHz offset	
Aging (F _{aging})		±1 ppm Max.	at 25°C, first year	
24 Hours Holdover Stability		±40 ppb Max.	Note [4]	
Free Run Stability for 20 Years		±4.6 ppm Max.	Note [5]	
Storage Temperature Range (T _{STR})		-55°C ~ +125°C		



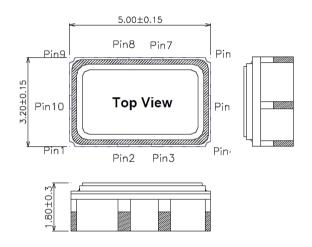
QTS532

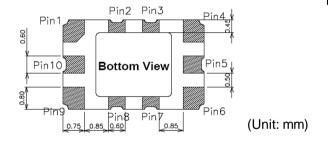
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Notes:

- [1] Refer to nominal frequency, operation after 2 times of reflow at 25°C.
- [2] Refer to (Fmax + Fmin)/2.
- [3] Decoupling capacitor is required in external circuit.
- [4] 24 hours at constant temerature after 48 hours operation
- [5] Inclusive of initial tolerence at 25°C, variations over temerature, supply voltage, load, reflow soldering and aging for 20 years.

Dimensions

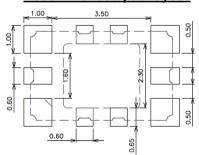




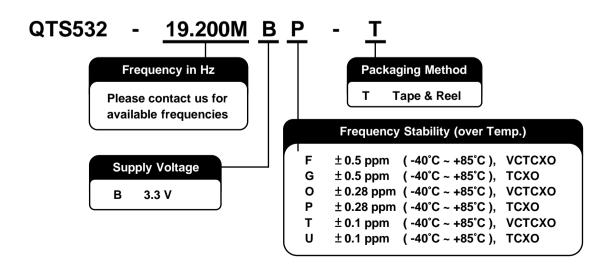
Pin function

Pin	Function			
FIII	TCXO	VCTCXO		
Pin 1	GND or NC	AFC		
Pin 2	Do not connection			
Pin 3	Tri-state			
Pin 4	GND			
Pin 5	Do not connection			
Pin 6	OUTPUT			
Pin 7	Do not connection			
Pin 8	Do not connection			
Pin 9	V_{DD}			
Pin 10	Do not connection			

Recommended pad layout



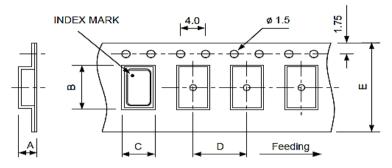
Ordering Information

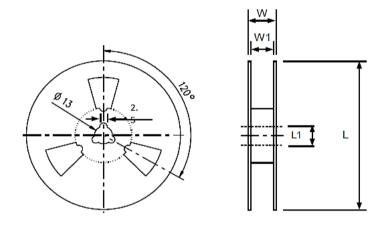


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Packing

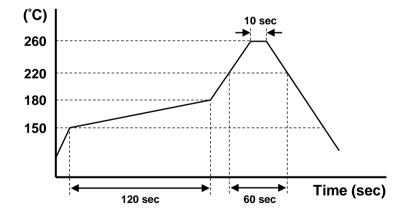




DIMENSIONS	Α	В	С	D	Е	L	L1	W	W1	
(mm)	2.20	5.30	3.50	8.00	12.00	178	17.3	16.1	13.5	

(Unit: mm)

Reflow Profile



Notes:

- [1] Period while temperature exceeds the solder melting point 220°C should be less than 200 sec.
- [2] Period while temperature stays at the top melting point 260°C should be less than 30 sec.