

DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
24V	15mΩ @ V _{GS} = 4.5V	9.9A
	20mΩ @ V _{GS} = 2.5V	8.6A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

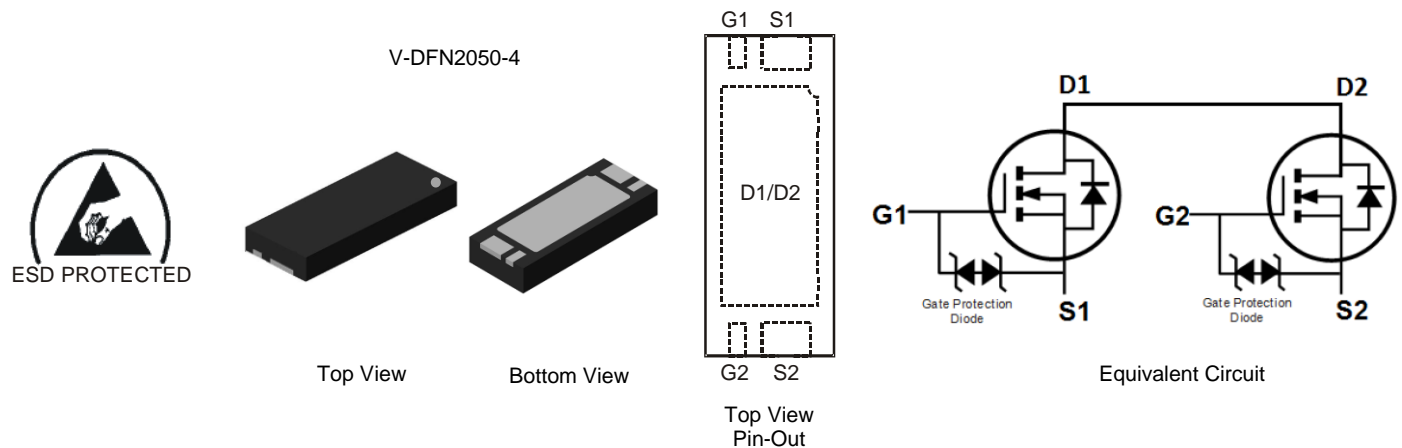
- General Purpose Interfacing Switch
- Power Management Functions

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**

Mechanical Data

- Case: V-DFN2050-4
- Case Material: Molded Plastic, "Green" Molding Compound.
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208④
- Weight: 0.01 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2016UFX-7	V-DFN2050-4	3,000 / Tape & Reel

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 - See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



CI9 = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: F = 2018)
M = Month (ex: 9 = September)

Date Code Key

Year	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028
Code	F	G	H	I	J	K	L	M	N	O	P

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	24	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 6) V _{GS} = 4.5V	Steady State	T _A = +25°C T _A = +70°C	I _D	9.9 7.9	A
Continuous Drain Current (Note 6) V _{GS} = 2.5V	Steady State	T _A = +25°C T _A = +70°C	I _D	8.6 6.9	A
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	3.0	A
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)			I _{DM}	80	A
Avalanche Current, L = 0.1mH (Note 7)			I _{AS}	20	A
Avalanche Energy, L = 0.1mH (Note 7)			E _{AS}	21	mJ

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Total Power Dissipation (Note 5)	P _D	1.07	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	R _{θJA}	117	°C/W
Total Power Dissipation (Note 6)	P _D	2.23	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 6)	R _{θJA}	56	°C/W
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	11	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	24	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	1	μA	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	μA	V _{GS} = ±12V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	0.4	0.6	1.5	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	9.1	15	mΩ	V _{GS} = 4.5V, I _D = 6.5A
			9.3	17		V _{GS} = 4.0V, I _D = 5.6A
			10.1	18		V _{GS} = 3.1V, I _D = 5.6A
			11.2	20		V _{GS} = 2.5V, I _D = 5.6A
			—	—		V _{GS} = 2.5V, I _D = 5.6A
Diode Forward Voltage	V _{SD}	—	0.6	0.9	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	950	—	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	130	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	120	—	pF	
Gate Resistance	R _g	—	1.3	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	—	14	—	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 8A
Gate-Source Charge	Q _{gs}	—	1.8	—	nC	
Gate-Drain Charge	Q _{gd}	—	6.5	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	3.8	—	ns	V _{DS} = 10V, I _D = 4A V _{GS} = 4.5V, R _G = 2Ω
Turn-On Rise Time	t _r	—	5.7	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	33	—	ns	
Turn-Off Fall Time	t _f	—	6.8	—	ns	I _F = 4A, dI/dt = 100A/μs
Body Diode Reverse Recovery Time	t _{RR}	—	9.3	—	ns	
Body Diode Reverse Recovery Charge	Q _{RR}	—	2.3	—	nC	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 - I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

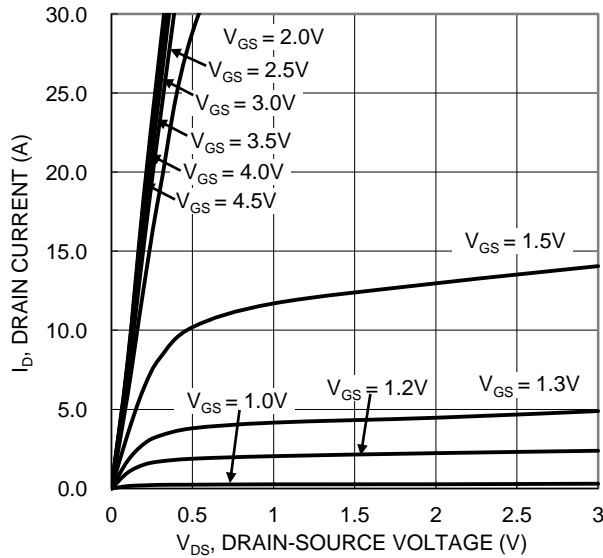


Figure 1. Typical Output Characteristic

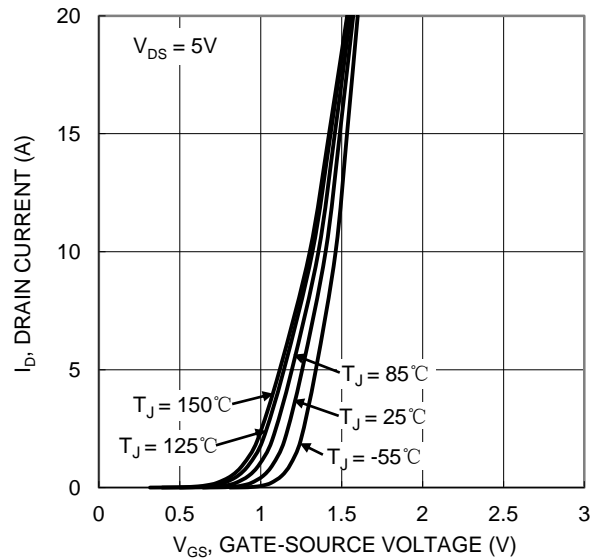


Figure 2. Typical Transfer Characteristic

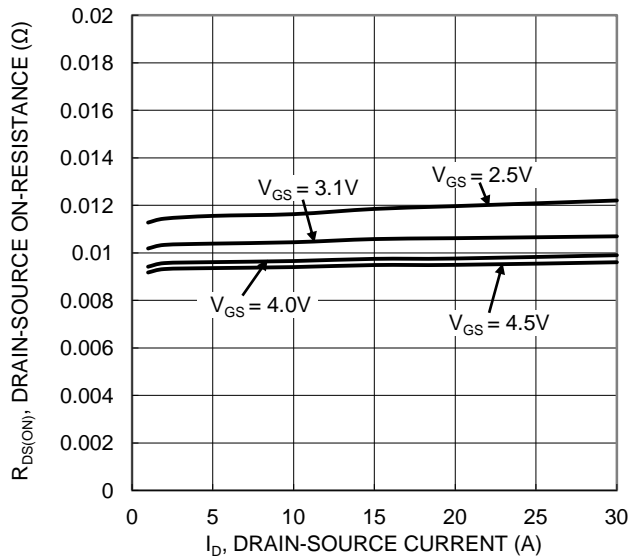


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

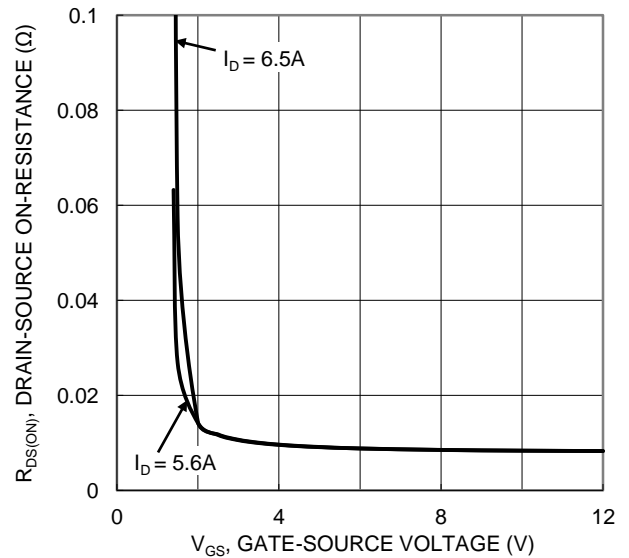


Figure 4. Typical Transfer Characteristic

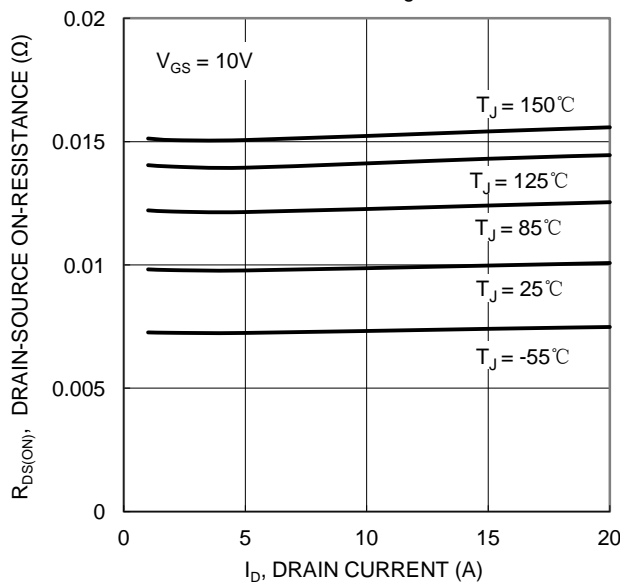


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

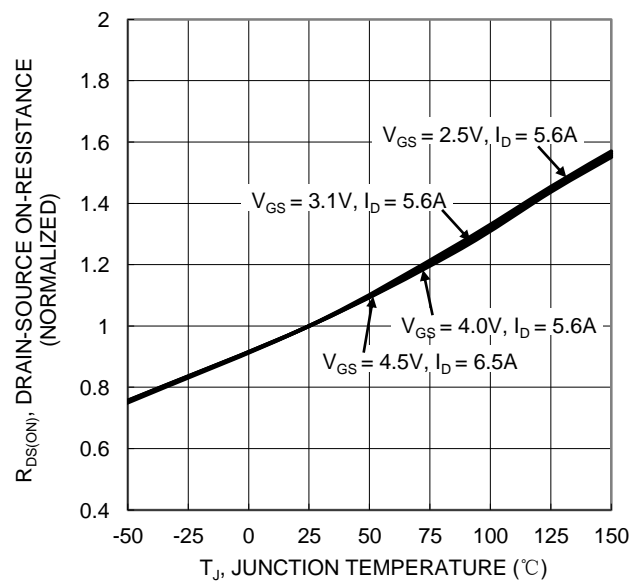


Figure 6. On-Resistance Variation with Junction Temperature

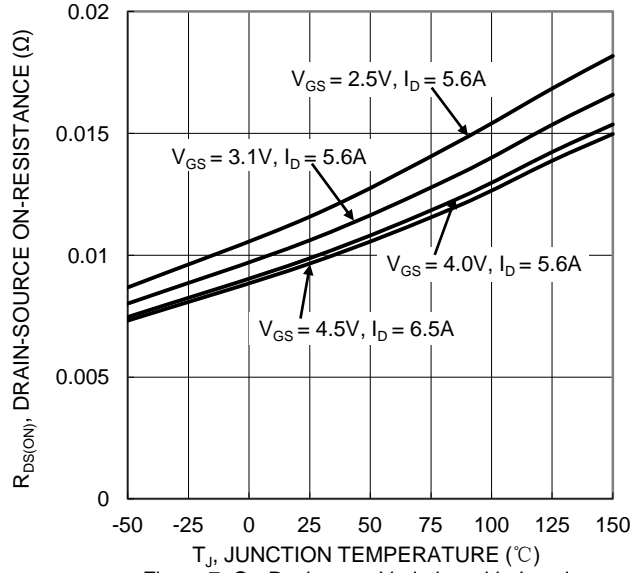


Figure 7. On-Resistance Variation with Junction Temperature

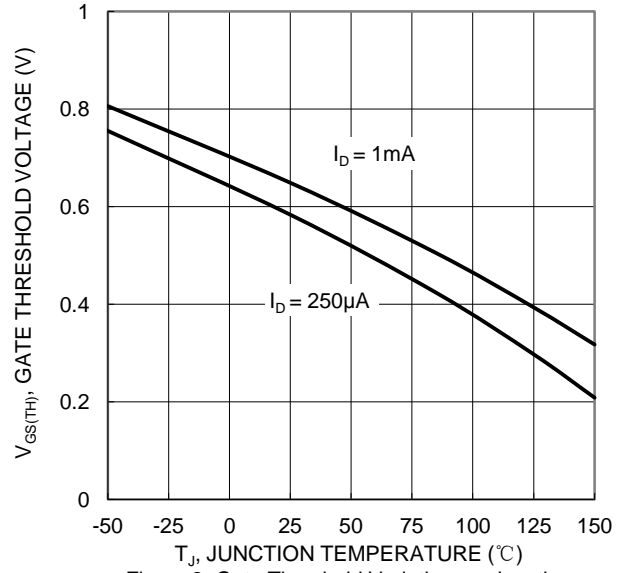


Figure 8. Gate Threshold Variation vs. Junction Temperature

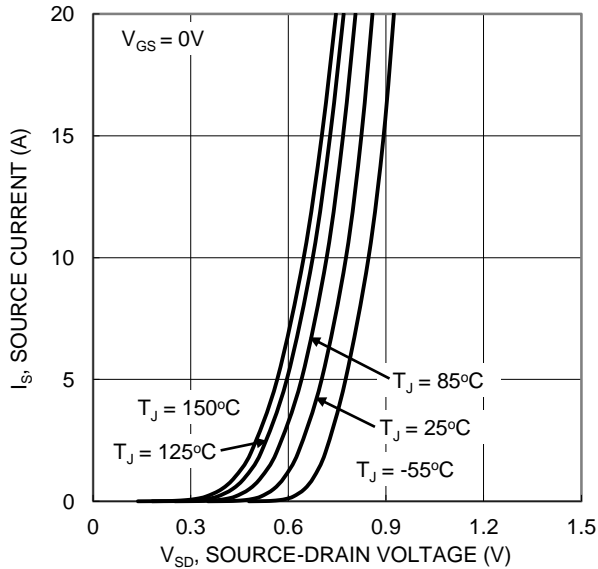


Figure 9. Diode Forward Voltage vs. Current

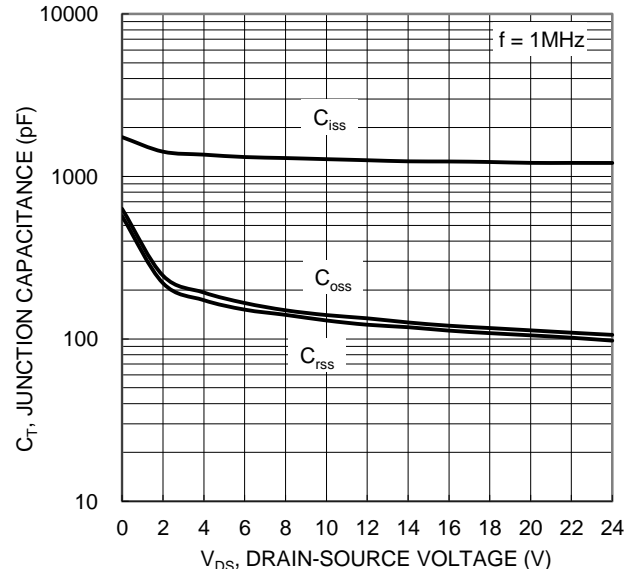


Figure 10. Typical Junction Capacitance

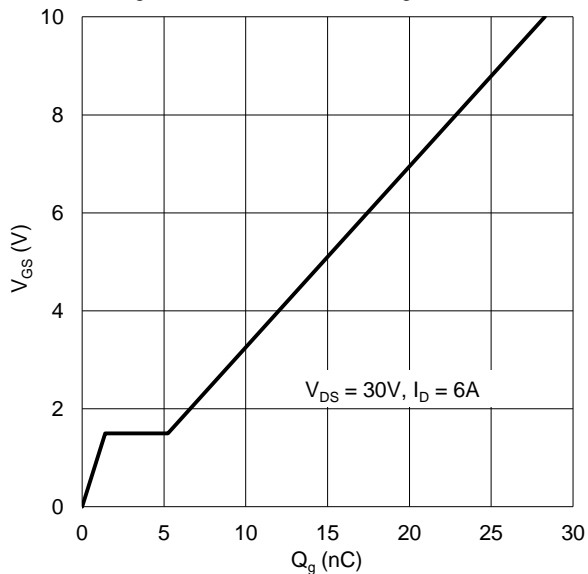


Figure 11. Gate Charge

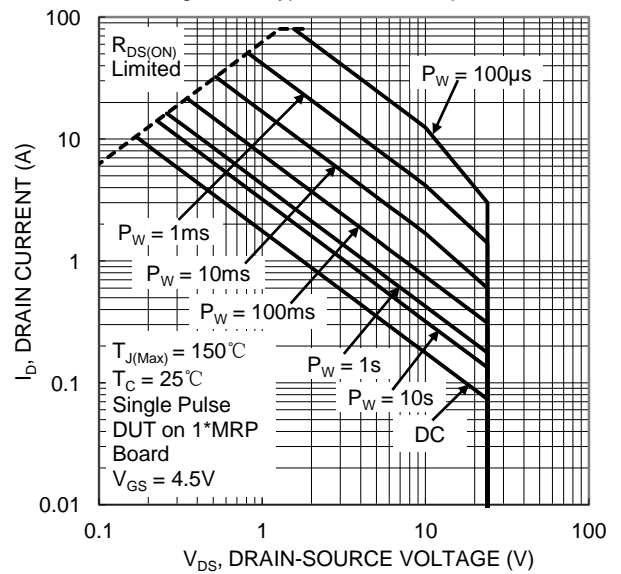


Figure 12. SOA, Safe Operation Area

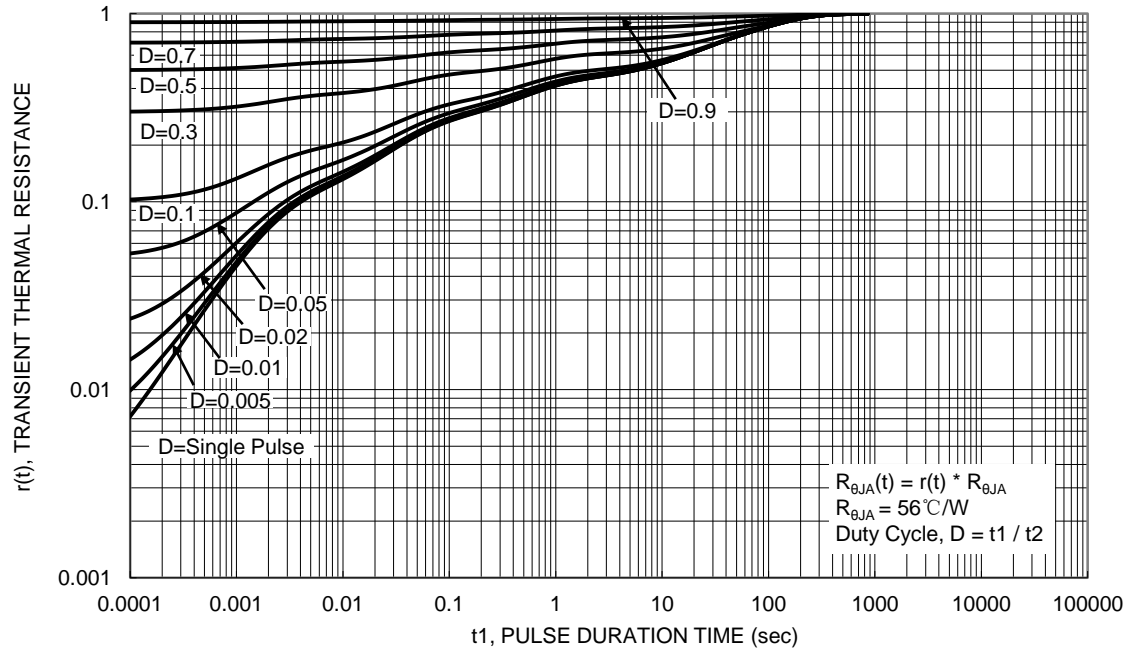
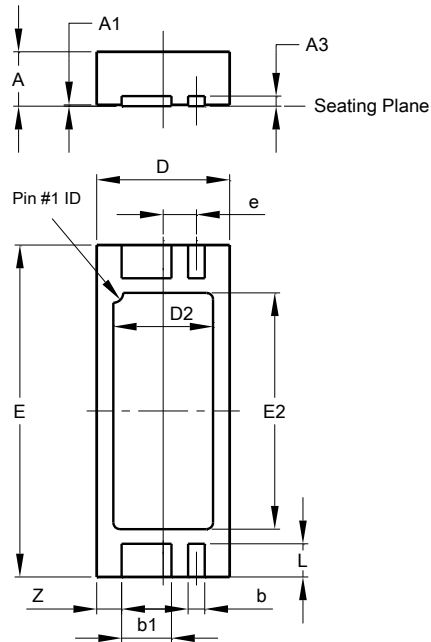


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

V-DFN2050-4

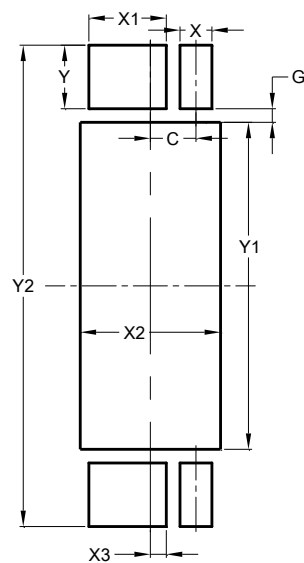


V-DFN2050-4			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0	0.05	0.02
A3	-	-	0.15
b	0.20	0.30	0.25
b1	0.70	0.80	0.75
D	1.90	2.10	2.00
D2	1.40	1.60	1.50
E	4.90	5.10	5.00
E2	3.46	3.66	3.56
e	0.50 BSC		
L	0.35	0.65	0.50
Z	-	-	0.375
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

V-DFN2050-4



Dimensions	Value (in mm)
C	0.500
G	0.150
X	0.350
X1	0.850
X2	1.540
X3	0.175
Y	0.700
Y1	3.600
Y2	5.300

IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2018, Diodes Incorporated

www.diodes.com