R5F71253N50FP

Products
Microcontroller and Microprocessor
SuperH Family
SH/Tiny Series
SH7124, SH7125
R5F71253N50FP

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Product Spec

Basic Information | Memory | CPU (Bit, Clock, DMA, External Bus etc) | Timers | Analog | Interfaces | Operating Conditions

Basic Information	
Product Status	Mass Production
Family	SuperH
Series	SH/Tiny(SH7125)
Group	SH7125

Memory	
Program memory	128 KB Flash memory
RAM	8 KB

CPU (Bit, Clock, DMA, External Bus etc)	
CPU	SH-2 (32-bit)
Max. Frequency	50 MHz
PLL	YES
Power-On Reset	YES
External Interrupt Pins	5
I/O Ports	45

Timers	
16-bit Timers	8 ch
Watchdog Timers	1 ch
PWM Output	16
3-Phase PWM Output Function	YES

Analog	
A/D Converters	10-bit x 8 ch

Interfaces	
CSIs	3 ch
UARTs	3 ch
Serial Interface Remarks	SCI (CSI:3ch/UART:3ch)

Operating Conditions	
Operating Voltage	4.0 to 5.5 V
Power Supply	Vcc=AVcc=4.0-5.5V
Operating Temperature	-20 to 85 °C (Ambient Temperature)

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Order Information

Orderable Part No.	Order	Production Status	Packing	EU RoHS	RoHS-pdf (China-RoHS)	Remark
DF71253N50FPV#Z1	Contact	Mass Production	Tray	Compliant	English	Consumer

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Package

Package	
Pins	64
Pin pitch	0.5 mm
Package Type	LFQFP
Body Size	10x10 mm^2
Package drawing	plqp0064kb_a
Mount pad drawing	fig0009e
Mounting height	1.7 mm
Mass	0.3 g
Renesas code	PLQP0064KB-A (old: FP-64K)
JEITA code	P-LFQFP64-10x10-0.50

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