RoHS

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Vishay Siliconix

# **Load Switch with Level-Shift**

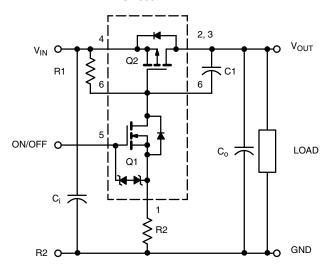


### Marking Code: VD

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	12					
$R_{DS(on)}(\Omega)$ at $V_{IN} = 4.5 \text{ V}$	0.200					
$R_{DS(on)}(\Omega)$ at $V_{IN} = 2.5 \text{ V}$	0.300					
$R_{DS(on)}$ ( $\Omega$ ) at $V_{IN} = 1.8 \text{ V}$	0.508					
I <sub>D</sub> (A)	± 1.1					
Configuration	Level-shift					

### **APPLICATION CIRCUITS**

#### Si1865DDL



COMPONENTS					
R1 Pull-up resistor Typical 10 kΩ to 1					
R2	Optional slew-rate control	Typical 0 to 100 kΩ a			
C1	Optional slew-rate control	Typical 1000 pF			

#### Note

a. Minimum R1 value should be at least 10 x R2 to ensure Q1 turn-on

#### **FEATURES**

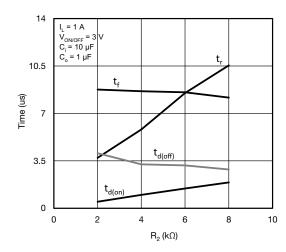
- Low R<sub>DS(on)</sub> TrenchFET®
- 1.8 V to 12 V input
- 1.5 V to 8 V logic level control
- · Low profile, small footprint SC-70-6 package
- 2000 V ESD protection on input switch, V<sub>ON/OFF</sub>
- Adjustable slew-rate
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **APPLICATIONS**

- · Load switch with level-shift
- Slew-rate control
- Portable / consumer devices

#### **DESCRIPTION**

The Si1865DDL includes a p- and n-channel MOSFET in a single SC-70-6 package. The low on-resistance p-channel TrenchFET is tailored for use as a load switch. The n-channel, with an external resistor, can be used as a level-shift to drive the p-channel load-switch. The n-channel MOSFET has internal ESD protection and can be driven by logic signals as low as 1.5 V. The Si1865DDL operates on supply lines from 1.8 V to 12 V, and can drive loads up to 1.1 A.

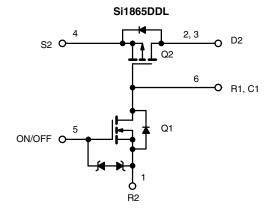


Switching Variation R2 at  $V_{IN}$  = 2.5 V, R1 = 20 k $\Omega$ 

The Si1865DDL is ideally suited for high-side load switching in portable applications. The integrated n-channel level-shift device saves space by reducing external components. The slew rate is set externally so that rise-times can be tailored to different load types.



### **FUNCTIONAL BLOCK DIAGRAM**



ORDERING INFORMATION				
Package	SC-70			
Lead (Pb)-free and halogen-free	Si1865DDL-T1-GE3			

ABSOLUTE MAXIMUM RATINGS	(T <sub>A</sub> = 25 °C, unless	otherwise no	ted)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Input voltage		$V_{IN}(V_{DS2})$	12	V	
On/off voltage		V <sub>ON/OFF</sub>	8	V	
Load aurrent	Continuous a, b	I.	± 1.1		
Load current	Pulsed b, c	IL	± 5	Α	
Continuous intrinsic diode conduction <sup>a</sup>		Is	-0.3		
Maximum power dissipation <sup>a</sup>		$P_{D}$	0.357	W	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C	
ESD rating, MIL-STD-883D human body model (100 pF, 1500 $\Omega$ )		ESD	2	kV	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient (continuous current) a	R <sub>thJA</sub>	290	350	°C/W
Maximum junction-to-foot (Q2)	R <sub>thJF</sub>	250	300	C/VV

SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT		
Off Characteristics								
Reverse leakage current	I <sub>FL</sub>	V <sub>IN</sub> = 12 V, V <sub>ON/OFF</sub> = 0 V	-	-	1	μA		
Diode forward voltage	$V_{SD}$	I <sub>S</sub> = -0.8 A	-	-0.84	-1.2	V		
On Characteristics								
Input voltage range	V <sub>IN</sub>		1.8	-	12	V		
On-resistance (p-channel)	R <sub>DS(on)</sub>	$V_{ON/OFF} = 1.5 \text{ V}, V_{IN} = 4.5 \text{ V}, I_D = 1.1 \text{ A}$	-	0.165	0.200			
		$V_{ON/OFF} = 1.5 \text{ V}, V_{IN} = 2.5 \text{ V}, I_D = 0.9 \text{ A}$	-	0.250	0.300	Ω		
		$V_{ON/OFF} = 1.5 \text{ V}, V_{IN} = 1.8 \text{ V}, I_D = 0.2 \text{ A}$	-	0.376	0.508			
On-state (p-channel) drain-current	I <sub>D(on)</sub>	$V_{IN-OUT} \le 0.2 \text{ V}, V_{IN} = 5 \text{ V}, V_{ON/OFF} = 1.5 \text{ V}$	1	-	-	Α		
		$V_{IN-OUT} \le 0.3 \text{ V}, V_{IN} = 3 \text{ V}, V_{ON/OFF} = 1.5 \text{ V}$	1	=.	=.	_ ^		

### Notes

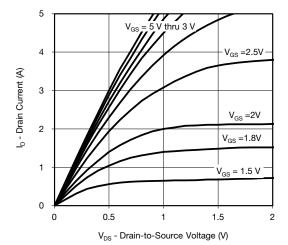
- a. Surface mounted on FR4 board
- b.  $V_{IN}$  = 12 V,  $V_{ON/OFF}$  = 8 V,  $T_A$  = 25 °C
- c. Pulse test: pulse width  $\leq 300~\mu s$ , duty cycle  $\leq 2~\%$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

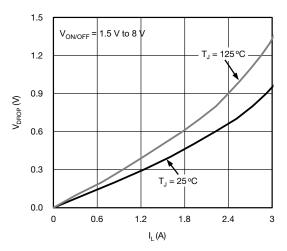
S13-2618-Rev. B, 23-Dec-13 **2** Document Number: 62888



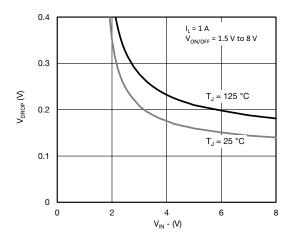
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



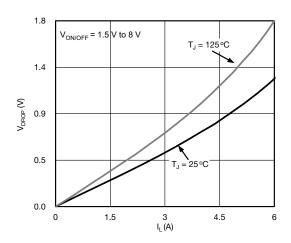
### **Output Characteristics**



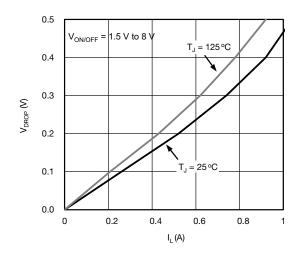
V<sub>DROP</sub> vs. I<sub>L</sub> at V<sub>IN</sub> = 2.5 V



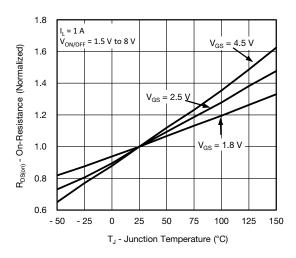
V<sub>DROP</sub> vs. V<sub>IN</sub> at I<sub>L</sub> = 1 A



 $V_{\mbox{\footnotesize DROP}}$  vs.  $I_L$  at  $V_{\mbox{\footnotesize IN}}$  = 4.5 V



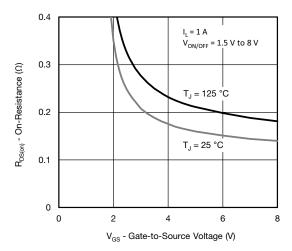
 $V_{DROP}$  vs.  $I_L$  at  $V_{IN}$  = 1.8 V



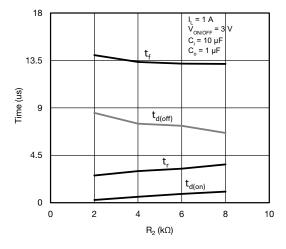
Normalized On-Resistance vs. Junction Temperature



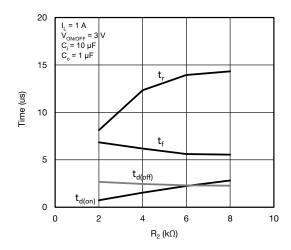
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



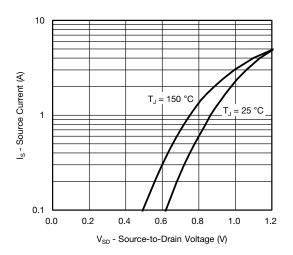
On-Resistance vs. Input Voltage



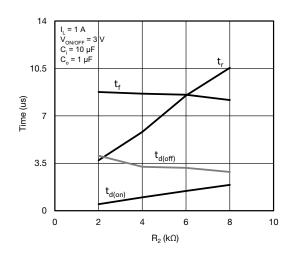
Switching Variation R2 at V $_{\rm IN}$  = 4.5 V, R1 = 20 k $\Omega$ 



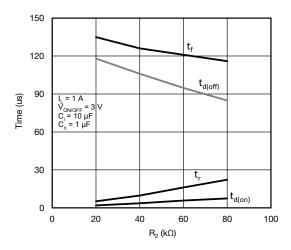
Switching Variation R2 at V<sub>IN</sub> = 1.8 V, R1 = 20 k $\Omega$ 



Source-Drain Diode Forward Voltage



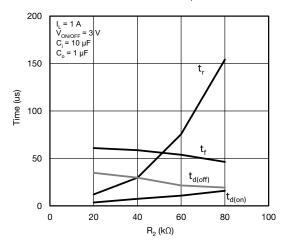
Switching Variation R2 at V<sub>IN</sub> = 2.5 V, R1 = 20 k $\Omega$ 

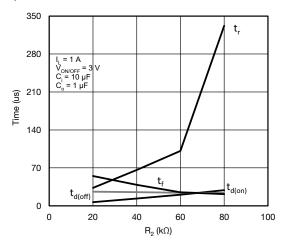


Switching Variation R2 at V<sub>IN</sub> = 4.5 V, R1 = 300 k $\Omega$ 



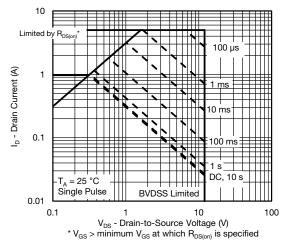
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



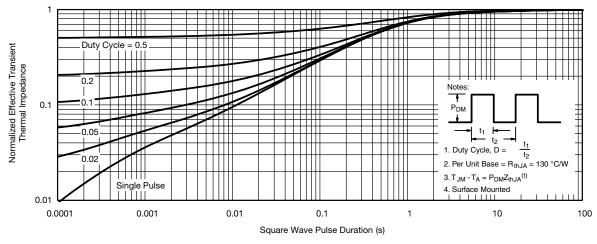


Switching Variation R2 at  $V_{IN}$  = 2.5 V, R1 = 300 k $\Omega$ 

Switching Variation R2 at V<sub>IN</sub> = 1.8 V, R1 = 300 k $\Omega$ 



Safe Operating Area, Junction-to-Foot

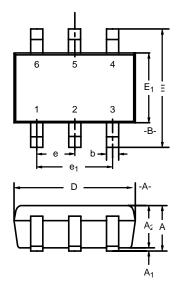


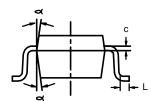
### Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?62888">www.vishay.com/ppg?62888</a>.



### **SC-70: 6-LEADS**





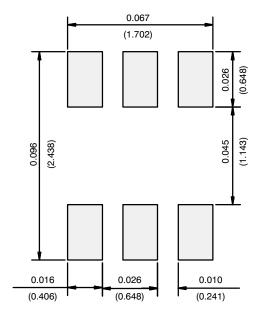
	MIL	LIMET	ERS	INCHES		
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.90	-	1.10	0.035	_	0.043
A <sub>1</sub>	-	-	0.10	-	-	0.004
$A_2$	0.80	-	1.00	0.031	-	0.039
b	0.15	-	0.30	0.006	-	0.012
С	0.10	-	0.25	0.004	_	0.010
D	1.80	2.00	2.20	0.071	0.079	0.087
Ε	1.80	2.10	2.40	0.071	0.083	0.094
E <sub>1</sub>	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65BSC				0.026BSC	;
e <sub>1</sub>	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
٦	7°Nom				7°Nom	

DWG: 5550

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### **RECOMMENDED MINIMUM PADS FOR SC-70: 6-Lead**



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOT

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