





20V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
20V	$0.99\Omega @ V_{GS} = 4.5V$	510mA
	$1.2\Omega @ V_{GS} = 2.5V$	470mA
	$1.8\Omega @ V_{GS} = 1.8V$	380mA
	2.4Ω @ $V_{GS} = 1.5V$	330mA

Description

This MOSFET has been designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

Features and Benefits

- Low Package Profile, 0.4mm Maximum Package height
- 0.48mm² package footprint, 16 times smaller than SOT23
- Low On-Resistance
- Very low Gate Threshold Voltage, 1.0V max
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

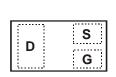
Mechanical Data

- Case: X2-DFN0806-3
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper leadframe.
 Solderable per MIL-STD-202, Method 208 64
- Weight: 0.001 grams (approximate)

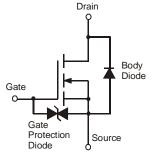








Top View Package Pin Configuration



Equivalent Circuit

Ordering Information (Note 4)

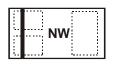
Part Number	Case	Packaging
DMN2990UFA-7B	X2-DFN0806-3	10K/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

DMN2990UFA-7B



Top View Bar Denotes Gate and Source Side

NW = Product Type Marking Code

June 2013

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Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	20	V		
Gate-Source Voltage	V_{GSS}	±8	V		
Continuous Drain Current (Note EVV 4.5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	510 410	mA
Continuous Drain Current (Note 5) V _{GS} = 4.5V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	610 490	mA
Continuous Drain Current (Note 5) V 1 9V	Steady State	$T_A = +25$ °C $T_A = 70$ °C	I _D	380 300	mA
Continuous Drain Current (Note 5) V _{GS} = 1.8V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	450 360	mA
Pulsed Drain Current (Note 6)	I _{DM}	800	mA		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	P _D	400	mW	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	р	310	°C/W
Thermal Resistance, sunction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	220	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current @T _C = +25°C	1	_	_	100	nA	$V_{DS} = 16V, V_{GS} = 0V$	
Zero Gate voltage Drain Current @1C = +25 C	I _{DSS}	_	_	50		$V_{DS} = 5V$, $V_{GS} = 0V$	
Gate-Source Leakage		_	_	±100	nA	$V_{GS} = \pm 5V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(th)}	0.4		1.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
		_	0.60	0.99		$V_{GS} = 4.5V, I_D = 100mA$	
		_	0.75	1.2		$V_{GS} = 2.5V, I_D = 50mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	0.90	1.8	Ω	$V_{GS} = 1.8V, I_D = 20mA$	
		_	1.2	2.4		$V_{GS} = 1.5V, I_D = 10mA$	
		_	2.0	_		V _{GS} = 1.2V, I _D = 1mA	
Forward Transfer Admittance		180	_	_	mS	V _{DS} = 10V, I _D = 400mA	
Diode Forward Voltage		-	0.6	1.0	V	V _{GS} = 0V, I _S = 150mA	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance		_	27.6	55.2	pF	101/11/101/	
Output Capacitance	Coss	_	4.0	8.0	pF	$V_{DS} = 16V, V_{GS} = 0V,$ -f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	2.8	5.6	pF	1 - 1.000112	
Total Gate Charge	Q_g	_	0.5	_	nC	\\ 4.5\\ \\ 10\\	
Gate-Source Charge		_	0.07	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_{D} = 250 \text{mA}$	
Gate-Drain Charge	Q_{gd}	_	0.07	_	nC	ID = ZOUIIA	
Turn-On Delay Time	t _{D(on)}	_	4.0	_	ns	14 401/14 451/	
Turn-On Rise Time		_	3.3	_	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	t _{D(off)}	_	19.0	_	ns	$R_L = 47\Omega$, $R_G = 10\Omega$,	
Turn-Off Fall Time	t _f	_	6.4	_	ns	$I_D = 200 \text{mA}$	

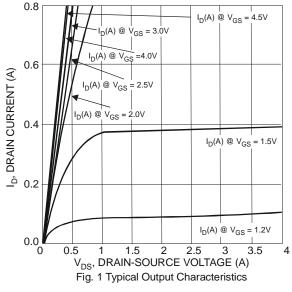
otes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.

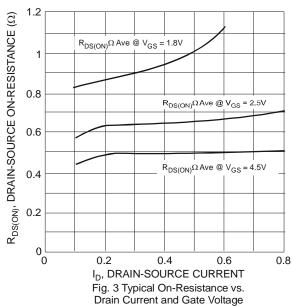
6. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.

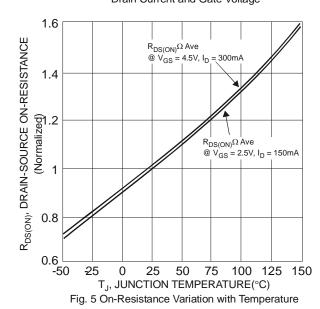
7. Short duration pulse test used to minimize self-heating effect.

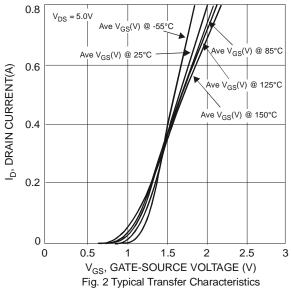
8. Guaranteed by design. Not subject to product testing.











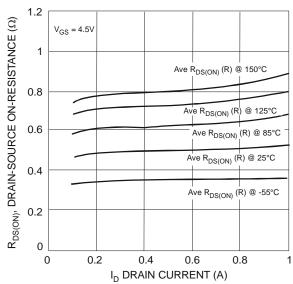


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

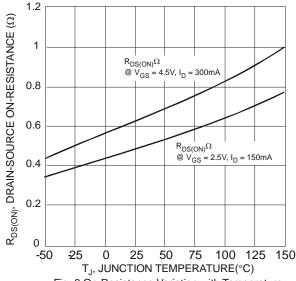


Fig. 6 On-Resistance Variation with Temperature



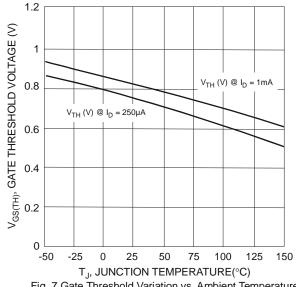
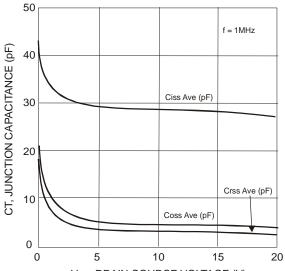


Fig. 7 Gate Threshold Variation vs. Ambient Temperature



 ${
m V_{DS}},$ DRAIN-SOURCE VOLTAGE (V) Fig. 9 Typical Junction Capacitance

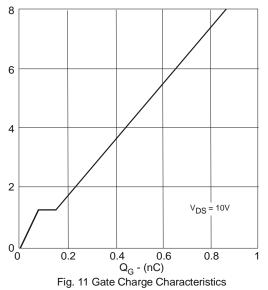
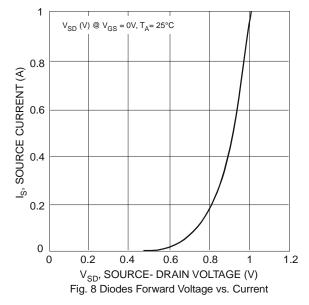


Fig. 11 Gate Charge Characteristics



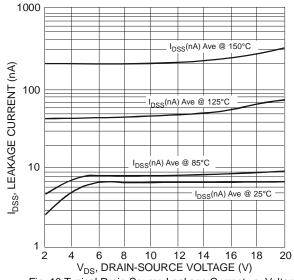
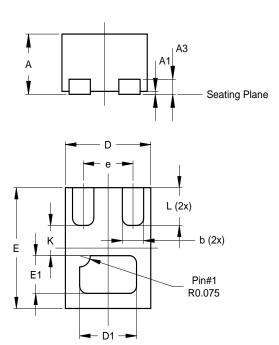


Fig. 10 Typical Drain-Source Leakage Current vs. Voltage



Package Outline Dimensions

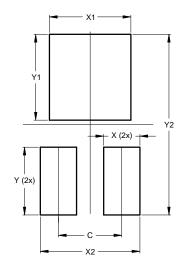
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



X2-DFN0806-3						
Dim	Min	Max	Тур			
Α	0.375	0.40	0.39			
A1	0	0.05	0.02			
A3	-	-	0.10			
b	0.10	0.20	0.15			
D	0.55	0.65	0.60			
D1	0.35	0.45	0.40			
Е	0.75	0.85	0.80			
E1	0.20	0.30	0.25			
е	-	-	0.35			
K	-	-	0.20			
١	0.20	0.30	0.25			
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value		
Dillielisions	(in mm)		
С	0.350		
Х	0.200		
X1	0.450		
X2	0.550		
Υ	0.375		
Y1	0.475		
Y2	1.000		



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