

Crystal Clear Technology

Product Specification

G128240x08xxx00

Crystal Clear Technology sdn. bhd.

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2.0 Record of revision

Rev	Date	Item	Page	Comment	Originator	Checked By
1.0	14.07.16			Initial Release	SCChong	Azhar



3.0 General specification

Display format: Graphics, 240 (H) x 128 (W)

Pixel size: 0.40 (H) x 0.40 (W) mm

Pixel pitch: 0.45 (H) x 0.45 (W) mm

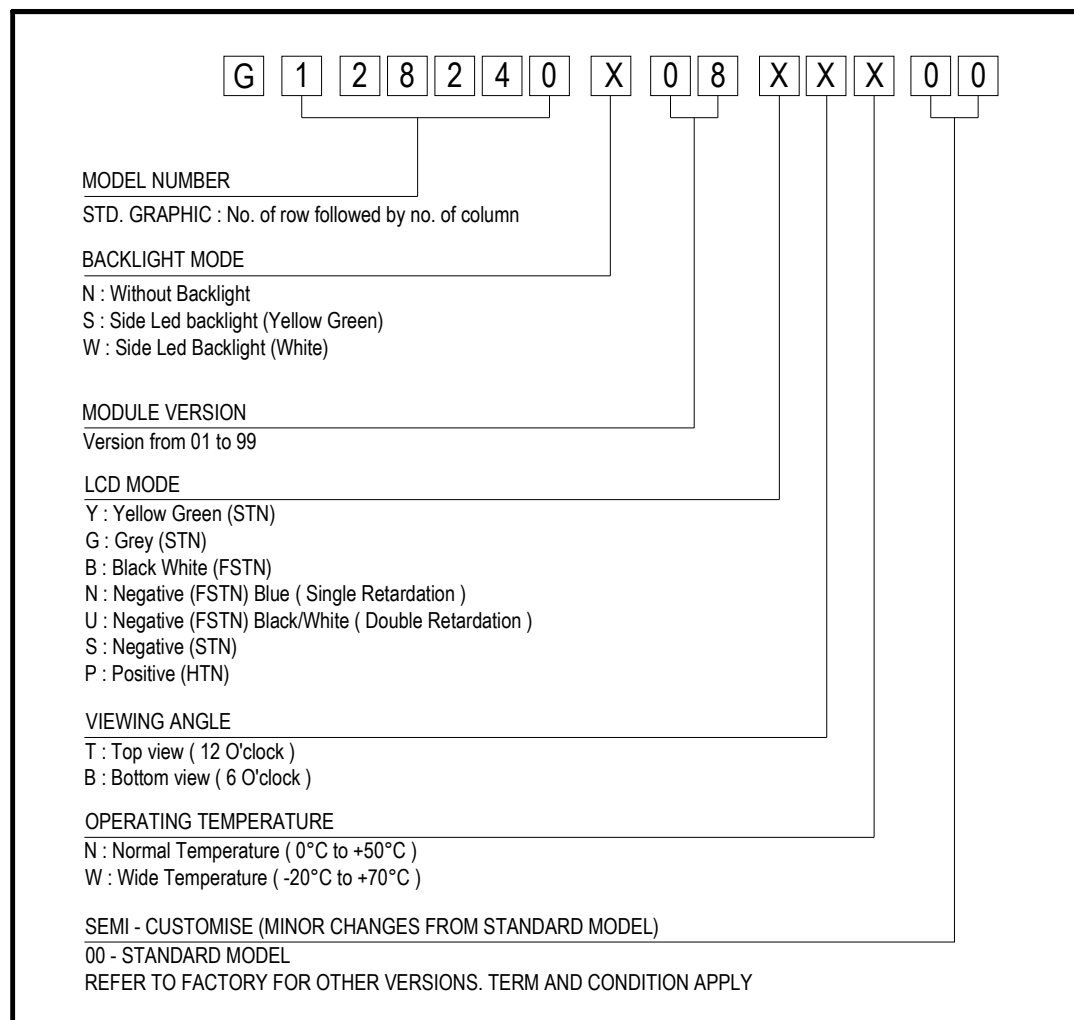
View area: 64.0 (H) x 114.0 (W) mm

Active area: 57.55 (H) x 107.95 (W) mm

General dimensions: 104.0 (H) x 144.0 (W) x 13.5 max (T) mm

Controller/Driver: UCi6963 and UCi0086 or equivalent

Interface: Parallel



**4.0 Absolute maximum rating (at $V_{SS} = 0V$, ambient temperature = 25°C)**

NO	ITEM	SIMBOL	MIN	MAX	UNIT
1.	Power Supply voltage (Logic)	$V_{DD} - V_{SS}$	-0.3	7.0	V
2.	Power Supply voltage (LCD Driver)	$V_{DD} - V_0$	-	19.0	V
3.	Operating Temperature	T_{op}	Refer page 3		°C
4.	Storage Temperature	T_{st}	Refer page 3		°C

5.0 Electrical characteristics

NO	ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
1.	Power Supply voltage (Logic)	$V_{DD} - V_{SS}$	-	4.5	5.0	5.5	V
2.	Power Supply voltage (V_{LCD})	$V_{DD} - V_0$	25°C	18.0±5%			V
3.	Input Voltage	V_{IH}	-	0.8 V_{DD}	-	V_{DD}	V
		V_{IL}	-	0	-	0.2 V_{DD}	V
4.	Current Supply	I_{DD}	$V_{DD} - V_{SS} = 5.0V$	-	17	20	mA

5.1 Backlight Options

NO	COLOR	FORWARD VOLTAGE (V)			FORWARD CURRENT (mA)			MIN BRIGHTNESS (cd/m ²) *
		Min	Typ.	Max	Min	Typ.	Max	
1.	Yellow Green	-	4.5	-	-	180	220	350
2.	White	-	5.0	-	-	120	160	550

*Note: 1. Brightness measured at backlight surface.

2. On LCD surface, brightness is only about 10% to 15% of backlight brightness.

3. Lifetime of backlight: 20k hrs (Conditions: $T_a = 25^\circ C$, I_f = typical supply)

6.0 Environmental requirements

NO	ITEM	CONDITION
1.	Operating Temperature	Refer page 3
2.	Storage Temperature	Refer page 3
3.	Operating Humidity	5% to 95%RH
4.	Cycle Test	0 C @ 30 min to 50 C @ 30min for 1 cycle run for 10 cycles
5.	Lifetime	50000 HOURS (excluding backlight)

Note: The background on LCD has the possibility to be changed in different temperature range.



7.0 LCD specification

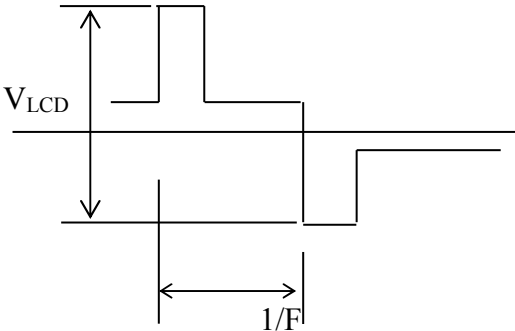
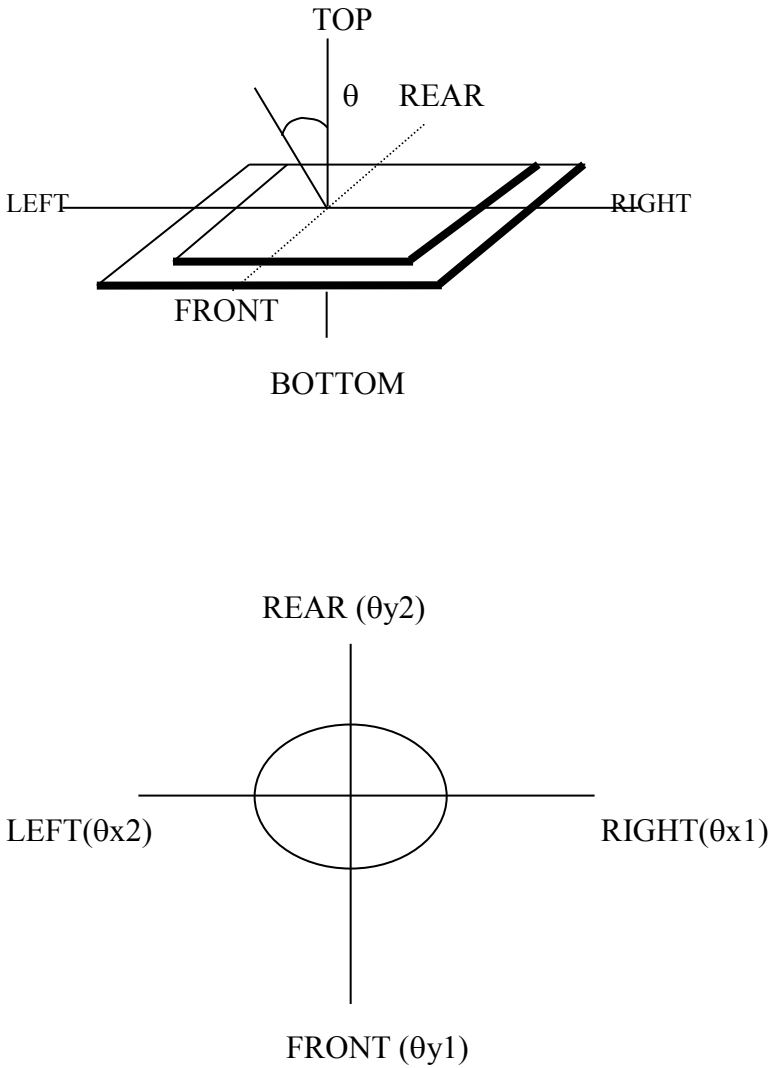
7.1 Electro-optical characteristics (at ambient temperature = 25°C)

NO	ITEM	SYMBOL	CONDITION	LCD TYPE							REF.
				STN YG	STN GREY	STN -VE BLUE/ PURP LE	FSTN +VE B/W	FSTN -VE BLUE	FSTN - VE TRUE B/W	FSTN -VE TRI AXIS	
1	Operating Voltage (Volt)	V _{LCD}	θ = 0 Cr = max	18.0 ± 5%							7.1.1
2	Viewing Angle (Deg)	θ x 1	CR ≥ 2 V _{LCD} = 18.0V	+20	+15	+35	+20	+35	+30	+40	7.1.2
		θ x 2		-20	-15	-35	-20	-35	-35	-40	
		θ y 1		-25	-20	-30	-25	-30	-30	-50	
		θ y 2		+25	+20	+30	+25	+30	+30	+30	
3	Contrast Ratio	CR	θ = 0 ⁰ V _{LCD} =18.0V	2.5	2.0	5.5	2.5	5.5	15	15	7.1.3
4	Response Time (msec)	Rise Time (Tr)	θ = 0 ⁰	400							7.1.4
		Decay Time (Td)	θ = 0 ⁰	400							

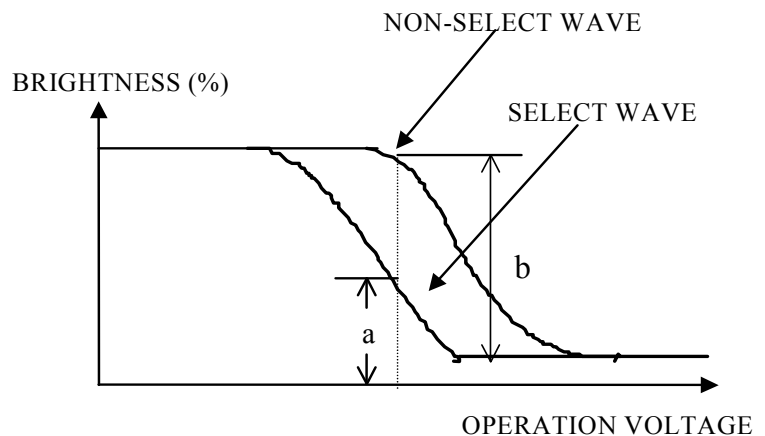
Note:

1. Viewing angle data is based on bottom view product by default. Should it be a top view product, values are then swap.
2. Contrast ratio is based on typical data when using white colour as backlight.
3. Equipment Used Eldim; Ez Contrast 120R , Spot Size = 2mm



NO	CHARACTERISTICS	DEFINITIONS
7.1.1	Definition of Operating Voltage (V_{LCD})	 V_{LCD} : Operating Voltage F : Frame Frequency
7.1.2	Definition of Viewing Angle	

7.1.3 *Definition of Contrast Ratio*

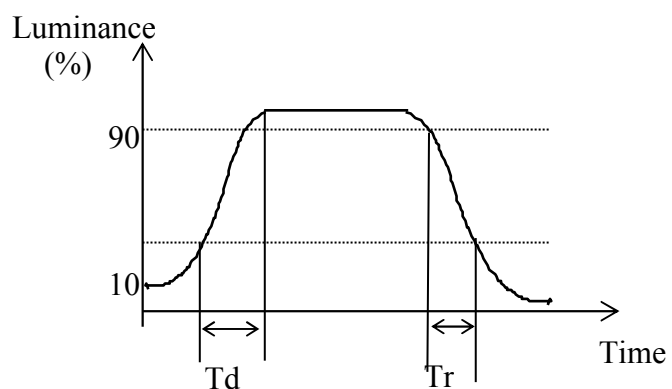


$$\text{Contrast Ratio} = \frac{\text{Brightness of non-selected state (b)}}{\text{Brightness of selected state (a)}}$$

Conditions

- (a) Operating Voltage: V_{LCD}
- (b) Temperature: 25°C
- (c) Viewing Angle, $\theta = 0^{\circ}$

7.1.4 *Response Time*



T_r : Measured between 10% and 90% of LCD segment maximum response with V_{ON} .

T_d : With voltage switches to zero and the instant LCD segment reaches 10% of its maximum response.



8.0 Interface

8.1	Controller	UCi6963	
8.2	Display Driver	UCi0086 or equivalent	
8.3	Duty Cycle	1/128	
8.4	Pin-out Assignments		
	Pin No	Symbol	Description
	1	V _{SS}	Ground Terminal of Module
	2	V _{DD}	Supply terminal of Module
	3	V ₀	Power supply for Liquid Crystal Drive
	4	C/D	Command/Data High = Command Register Low = Data Register
	5	/RD	Read Signal (active low)
	6	/WR	Write Signal (active low)
	7	DB0	Data Bus
	8	DB1	Data Bus
	9	DB2	Data Bus
	10	DB3	Data Bus
	11	DB4	Data Bus
	12	DB5	Data Bus
	13	DB6	Data Bus
	14	DB7	Data Bus
	15	/CE	Chip Enable Signal
	16	/RESET	Reset signal (active Low)
	17	VEE	Negative Voltage
	18	MD2	Control signal
	19	FS1	Font Selection (please refer to table below)
	20	NC	Not connected
	21	BL+	Backlight Voltage supply
	22	BL-	Backlight Ground terminal

***Font interface format selection :**

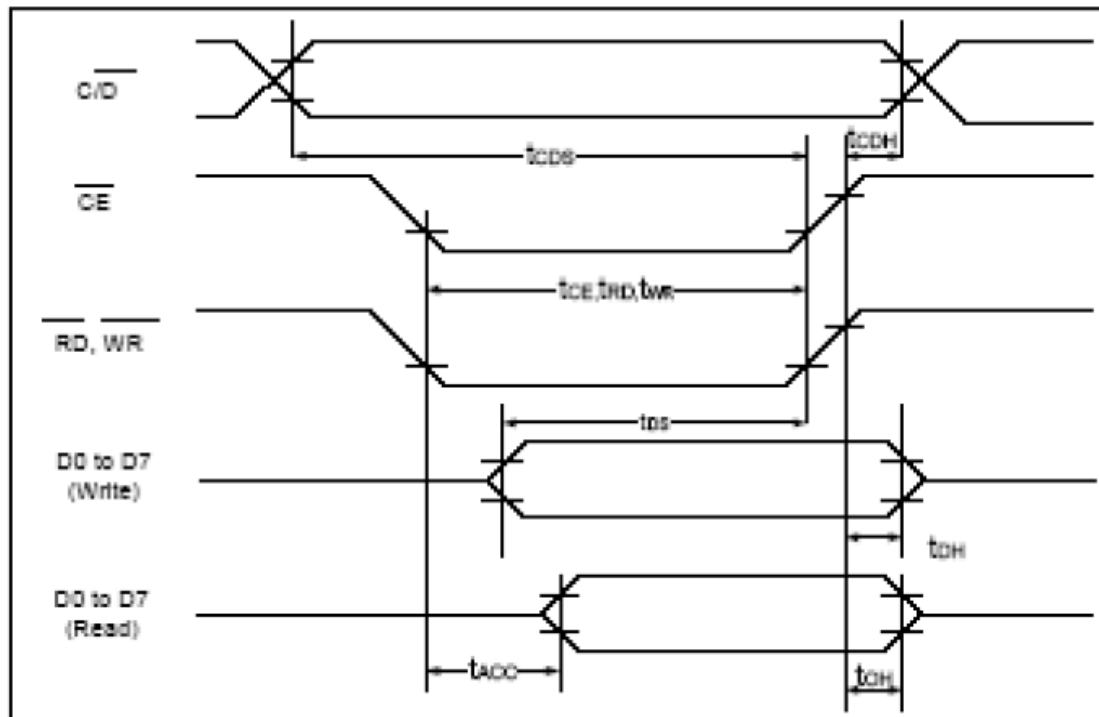
Font size	J1	J2	Pin FS1
5x8	Used	NC	High
6x8	NC*	Used*	High
7x8	Used	NC	Low
8x8	NC*	Used*	Low

Note: NC = Not Connected, * = Factory default settings.



9.0 Timing characteristics / Timing diagrams

9.1 Display Control Timing Waveform and Characteristics

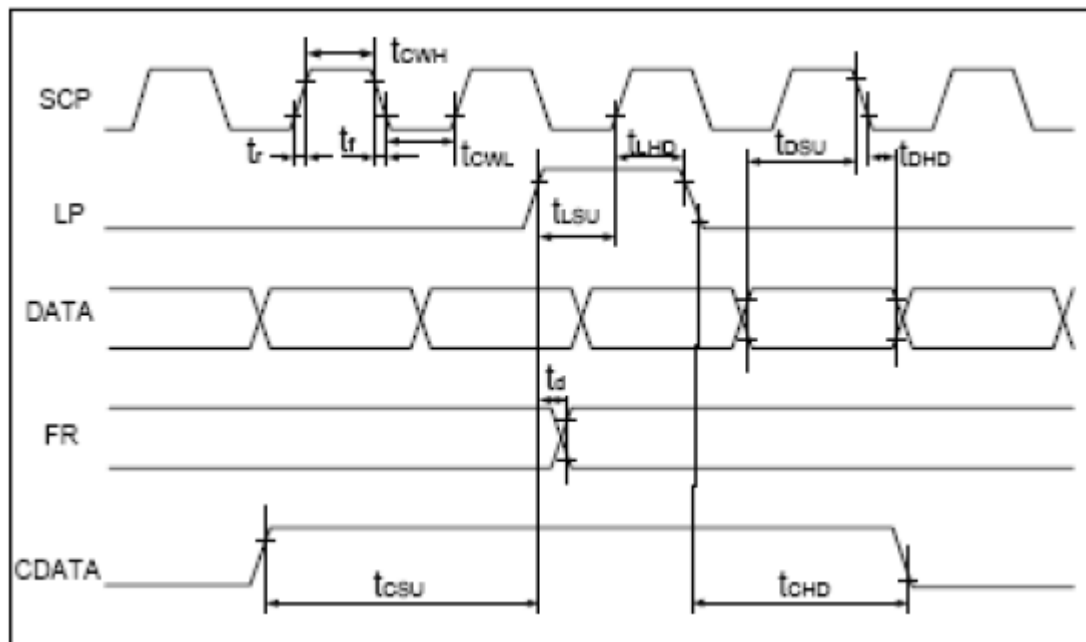


($V_{DD}=+5V\pm5\%$, $GND=0V$, $T_a = -20$ to $+70^\circ C$)

Item	Symbol	Test Conditions	Min.	Max.	Unit
$C/\overline{D}$ Set Up Time	t_{CDS}	--	100	--	ns
$C/\overline{D}$ Hold Time	t_{CDH}	--	10	--	ns
$\overline{CE}$, $\overline{RD}$, $\overline{WR}$ Pulse Width	t_{CE}, t_{RD}, t_{WR}	--	80	--	ns
Data Set Up Time	t_{DS}	--	80	--	ns
Data Hold Time	t_{OH}	--	40	--	ns
Access Time	t_{ACC}	--	--	150	ns
Output Hold Time	t_{OH}	--	10	50	ns



9.2 Driver Interface Timing



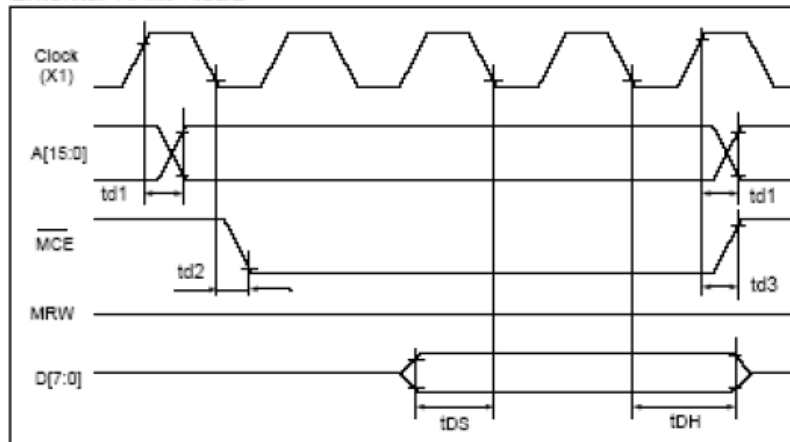
($V_{DD}=+5V\pm5\%$, $GND=0V$, $T_a = -20$ to $+70^\circ\text{C}$)

Item	Symbol	Test Conditions	Min.	Max.	Unit
Operating Frequency	f_{SCP}	$T_a = -20\sim70^\circ\text{C}$	--	9	MHz
SCP Pulse Width	t_{CWH}, t_{CWL}	--	150	--	ns
SCP Rise/Fall Time	t_r, t_f	--	--	30	ns
LP Setup Time	t_{LSU}	--	150	290	ns
LP Hold Time	t_{LHD}	--	5	40	ns
Data Setup Time	t_{DSU}	--	170	--	ns
Data Hold Time	t_{DHD}	--	80	--	ns
FR Delay Time	t_d	--	0	90	ns
CDATA Setup Time	t_{CSU}	--	450	850	ns
CDATA Hold Time	t_{CHD}	--	450	950	ns

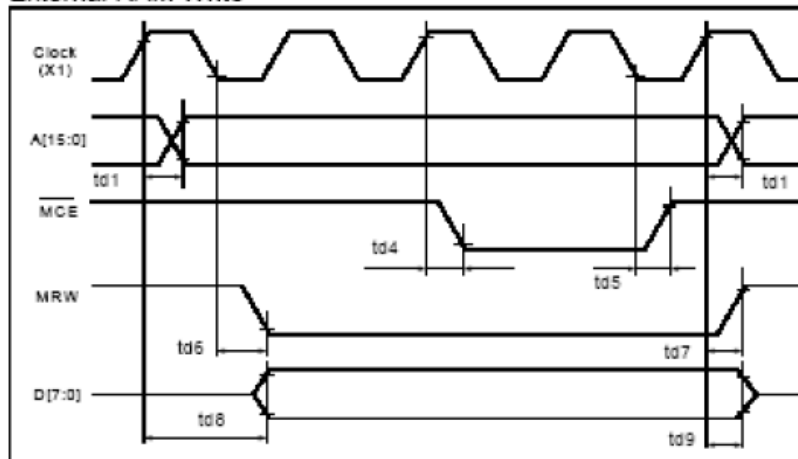


9.3 External Memory Interface

External RAM Read



External RAM Write

($V_{DD}=+5V\pm5\%$, GND=0V, $T_a = -20$ to $+70^\circ\text{C}$)

Item	Symbol	Test Conditions	Min.	Max.	Unit
Address Delay Time	t_{d1}	--	--	250	ns
MCE Fall Delay Time(Read)	t_{d2}	--	--	180	ns
MCE Rise Delay Time(Read)	t_{d3}	--	--	180	ns
Data Setup Time	t_{DS}	--	0	--	ns
Data Hold Time	t_{DH}	--	30	--	ns
MCE Fall Delay Time(Write)	t_{d4}	--	--	200	ns
MCE Rise Delay Time(Write)	t_{d5}	--	--	200	ns
MRW Fall Delay Time	t_{d6}	--	--	180	ns
MRW Rise Delay Time	t_{d7}	--	--	180	ns
Data Stable Time	t_{d8}	--	--	450	ns
Data Hold Time	t_{d9}	--	--	200	ns



9.4 Relationship between character code and pattern

CG ROM TYPE 0101

CGROM Font - 01

LSB MSB	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0		!	"	#	\$	%	&	'	(	)	*	+	,	-	.	/
1	0	1	2	3	4	5	6	7	8	9	:	;	<	=	>	?
2	@	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O
3	P	Q	R	S	T	U	V	W	X	Y	Z	[	\	]	^	_
4	`	a	b	c	d	e	f	g	h	i	j	k	l	m	n	o
5	p	q	r	s	t	u	v	w	x	y	z	{		}	~	
6	Q	ü	é	ä	ä	ä	ä	ö	ö	ö	ë	i	i	i	Ä	Ä
7	É	æ	Æ	ö	ö	ö	ö	ü	ü	ü	ö	ö	£	¥	ℳ	ℳ

CG ROM TYPE 0201

CGROM Font - 02

LSB MSB	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0		!	"	#	\$	%	&	'	(	)	*	+	,	-	.	/
1	0	1	2	3	4	5	6	7	8	9	:	;	<	=	>	?
2	@	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O
3	P	Q	R	S	T	U	V	W	X	Y	Z	[	¥	]	^	_
4	千	万	円	+	+	■	ヲ	フ	イ	ウ	エ	オ	カ	ユ	ヨ	ツ
5	一	ア	イ	ウ	エ	オ	カ	キ	ク	ケ	コ	サ	シ	ス	セ	ソ
6	タ	チ	ツ	テ	ト	ナ	ニ	ヌ	ネ	ノ	ハ	ヒ	フ	ヘ	ベ	セ
7	ミ	ム	メ	モ	ヤ	ユ	ヨ	ラ	リ	ル	レ	ロ	ワ	ウ	エ	オ



10. Instruction Set

C/D: 1: Command / 0: Data

W/R: 0: Write Cycle / 1: Read Cycle

D7-D0: -: Don't Care / #: Valid data

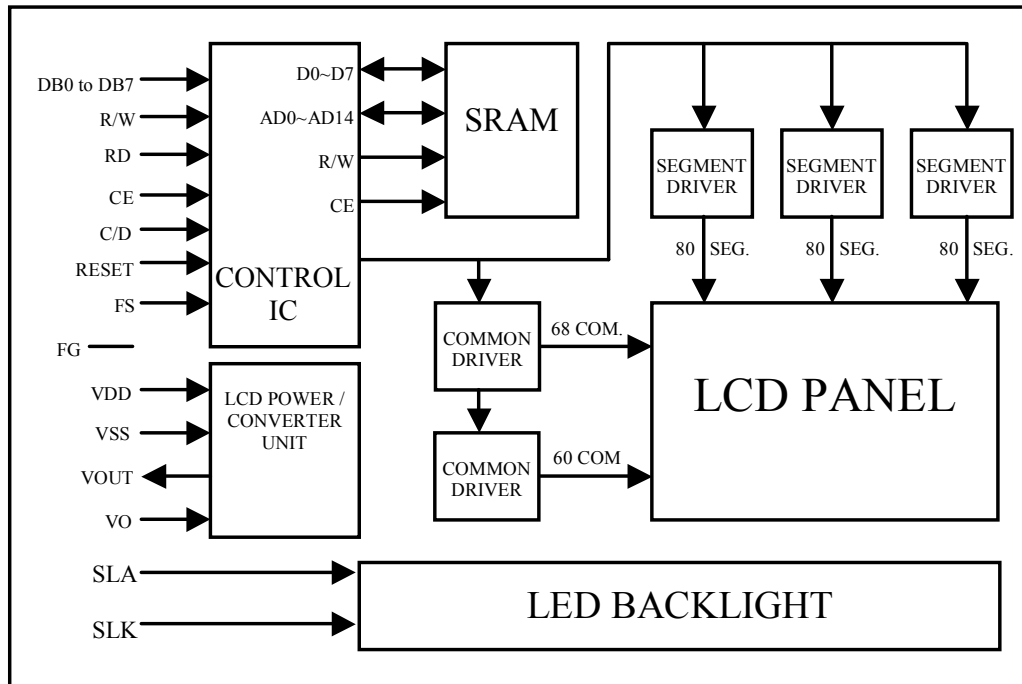
No.	Command	C/D	W/R	D7	D6	D5	D4	D3	D2	D1	D0	Action	Value
1.	Set Cursor Pointer	1	0	0	0	1	0	0	0	0	1		21h
		0	0	#	#	#	#	#	#	#	#	Set X address	
		0	0	#	#	#	#	#	#	#	#	Set Y address	
2.	Set Offset Register	1	0	0	0	1	0	0	0	1	0		22h
		0	0	#	#	#	#	#	#	#	#	Data	
		0	0	0	0	0	0	0	0	0	0		00h
3.	Set Address Pointer	1	0	0	0	1	0	0	0	1	0		24h
		0	0	#	#	#	#	#	#	#	#	Low address	
		0	0	#	#	#	#	#	#	#	#	High address	
4.	Set Text Home Addr.	1	0	0	1	0	0	0	0	0	0		40h
		0	0	#	#	#	#	#	#	#	#	Low address	
		0	0	#	#	#	#	#	#	#	#	High address	
5.	Set Text Area	1	0	0	1	0	0	0	0	0	1		41h
		0	0	#	#	#	#	#	#	#	#	Columns	
		0	0	#	#	#	#	#	#	#	#		
6.	Set Graphic Home Addr.	1	0	0	1	0	0	0	0	1	0		42h
		0	0	#	#	#	#	#	#	#	#	Low address	
		0	0	#	#	#	#	#	#	#	#	High address	
7.	Set Graphic Area	1	0	0	1	0	0	0	0	1	1		43h
		0	0	#	#	#	#	#	#	#	#	Columns	
		0	0	#	#	#	#	#	#	#	#		
8.	OR mode	1	0	1	0	0	0	-	0	0	0		8xh
9.	EXOR mode	1	0	1	0	0	0	-	0	0	1		
10.	AND mode	1	0	1	0	0	0	-	0	1	1		
11.	Text Attribute mode	1	0	1	0	0	0	-	1	0	0		
12.	Internal CG ROM mode	1	0	1	0	0	0	0	-	-	-		
13.	External CG RAM mode	1	0	1	0	0	0	1	-	-	-		9xh
14.	Display OFF	1	0	1	0	0	1	0	0	0	0		
15.	Cursor ON, Blink OFF	1	0	1	0	0	1	-	-	1	0		
16.	Cursor ON, Blink ON	1	0	1	0	0	1	-	-	1	1		
17.	Text ON, Graphic OFF	1	0	1	0	0	1	0	1	-	-		
18.	Text OFF, Graphic ON	1	0	1	0	0	1	1	0	-	-		
19.	Text ON, Graphic ON	1	0	1	0	0	1	1	1	-	-		A0h-A7h
20.	Cursor Pattern Select: 1-line cursor	1	0	1	0	1	0	0	0	0	0		
21.	Cursor Pattern Select: 2-line cursor	1	0	1	0	1	0	0	0	0	1		
22.	Cursor Pattern Select: 3-line cursor	1	0	1	0	1	0	0	0	1	0		
23.	Cursor Pattern Select: 4-line cursor	1	0	1	0	1	0	0	0	1	1		
24.	Cursor Pattern Select: 5-line cursor	1	0	1	0	1	0	0	1	0	0		
25.	Cursor Pattern Select: 6-line cursor	1	0	1	0	1	0	0	1	0	1		
26.	Cursor Pattern Select: 7-line cursor	1	0	1	0	1	0	0	1	1	0		
27.	Cursor Pattern Select: 8-line cursor	1	0	1	0	1	0	0	1	1	1		
28.	Data-write and Increase ADP	1	0	1	1	0	0	0	0	0	0		C0h
		0	0	#	#	#	#	#	#	#	#	Data	
29.	Data-read and Increase ADP	1	0	1	1	0	0	0	0	0	1		C1h
30.	Data-write and Decrease ADP	1	0	1	1	0	0	0	0	1	0		C2h
		0	0	#	#	#	#	#	#	#	#	Data	
31.	Data-read and Decrease ADP	1	0	1	1	0	0	0	0	1	1		C3h
32.	Data-write and Non-variable ADP	1	0	1	1	0	0	0	1	0	0		C4h
		0	0	#	#	#	#	#	#	#	#	Data	
33.	Data-read and Non-variable ADP	0	0	1	1	0	0	0	1	0	1		C5h
34.	Set Data Auto Write	1	0	1	0	1	1	0	0	0	0		B0h
35.	Set Data Auto Read	0	0	1	0	1	1	0	0	0	1		B1h
36.	Auto Reset	0	0	1	0	1	1	0	0	1	0		B2h
37.	Screen Peek	1	0	1	1	1	0	0	0	0	0		E0h



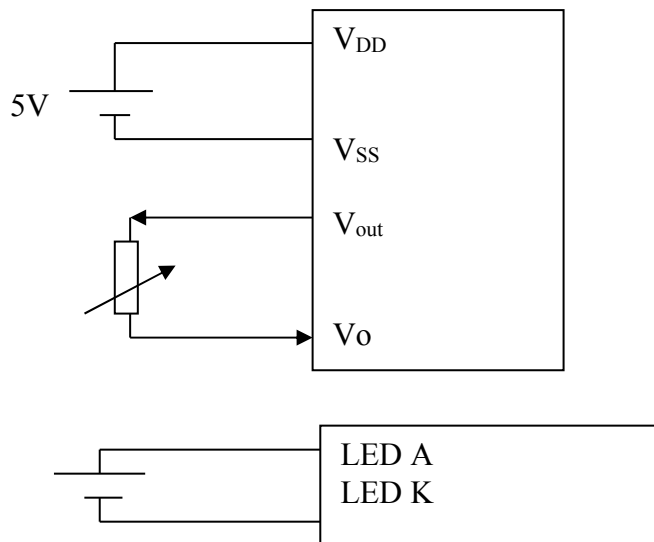
No.	Command	C/D	W/R	D7	D6	D5	D4	D3	D2	D1	D0	Action	Value
38.	Screen Copy	1	0	1	1	1	0	1	0	0	0		E8h
39.	Bit Reset	1	0	1	1	1	1	0	-	-	-		Fyh
40.	Bit Set	1	0	1	1	1	1	1	-	-	-		
41.	Bit 0	1	0	1	1	1	1	-	0	0	0		
42.	Bit 1	1	0	1	1	1	1	-	0	0	1		
43.	Bit 2	1	0	1	1	1	1	-	0	1	0		
44.	Bit 3	1	0	1	1	1	1	-	0	1	1		
45.	Bit 4	1	0	1	1	1	1	-	1	0	0		
46.	Bit 5	1	0	1	1	1	1	-	1	0	1		
47.	Bit 6	1	0	1	1	1	1	-	1	1	0		
48.	Bit 7	1	0	1	1	1	1	-	1	1	1		
49.	Whole Screen Reverse (Triple-byte command)	1	0	1	1	0	1	0	0	0	0		D0h
		0	0	-	-	-	-	-	-	-	#	0: Normal 1: Reverse	
		0	0	-	-	-	-	-	-	-	-	(Don't Care)	
50.	Blink Time (Triple-byte command)	1	0	0	1	0	1	0	0	0	0		50h
		0	0	-	-	-	-	-	#	#	#	000b: 0.086s 100b: 1s 001b: 0.25s 101b: 1.25s 010b: 0.5s 110b: 1.5s 011b: 1.75s 111b: 2s	010b
		0	0	-	-	-	-	-	-	-	-	(Don't Care)	
		0	0	-	-	-	-	-	-	-	-	(Don't Care)	
51.	Cursor Auto Moving (Triple-byte command)	1	0	0	1	1	0	0	0	0	0		60h
		0	0	-	-	-	-	-	-	-	#	0: disable 1: enable	
		0	0	-	-	-	-	-	-	-	-	(Don't Care)	
52.	CGROM Font Select (Triple-byte command)	1	0	0	1	1	1	0	0	0	0		70h
		0	0	-	-	-	-	-	-	#	#	00b: Don't care 01b: Don't care 10b: CGROM Font-01 11b: CGROM Font-01	
		1	0	-	-	-	-	-	-	-	-	(Don't Care)	
		1	0	-	-	-	-	-	-	-	-	(Don't Care)	



11. Block Diagram and Power Supply



Block Diagram

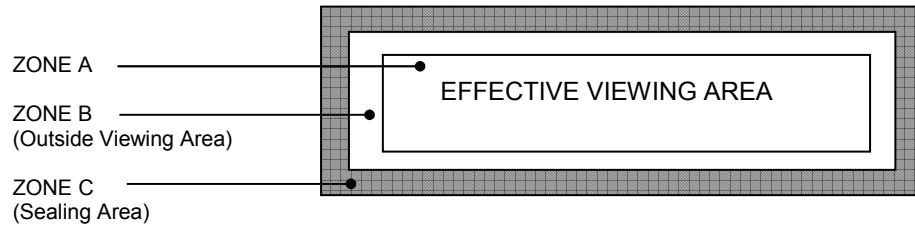


Power Supply



12.0 Quality Assurance

12.1 ZONE DEFINITION



12.2 REJECTION CRITERIA

12.2.1 DIMENSIONAL DEFECTS

Defect Category	Defect Description	Criterion	Drawing Specification
Glass Size	Dimensions of LCD, do not conform to the drawing	Reject	Refer to LCD Physical Dimension Drawing
Perimeter Seal Extension	Perimeter seal epoxy enters the effective viewing area	Reject	
End Seal Size	Size of end seal does not meet drawing specification	Reject	Refer to LCD Physical Dimension Drawing

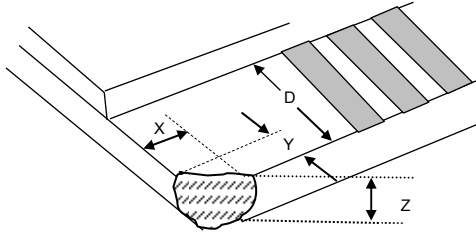
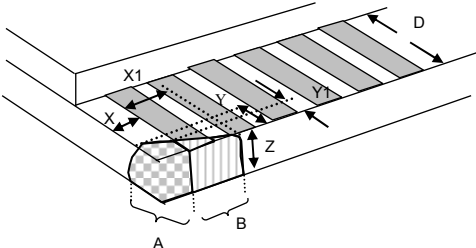
12.2.2 VISUAL DEFECTS

Defect Category	Defect Description	Criterion	Drawing Specification
Fracture	A type of glass breakage containing running cracks. Inspectors should attempt to remove it with fingernail. If removed, evaluate as chip	Reject – if the size is $\geq 30\%$ of the contact ledge width.	



Defect Category	Defect Description	Criterion	Drawing Specification
Chip	Chip in cross over area	1) Reject - if the chip causes crossover dot to be exposed 2) Chip on outside edge of the glass plate but is greater than 50% of glass thickness at crossover dot is reject able.	
Chip	Chip in contact pad area	Accept if:- a) $X \leq 2.0\text{mm}$ b) $Y \leq 0.5\text{mm}$ c) Z disregard	
	Chip in non-contact pad area	Accept if:- a) $X \leq 6.0\text{mm}$ b) $Y \leq 1.0\text{mm}$ c) Z disregard	
	Chip in perimeter seal area	Accept if:- a) $Y \leq 1/3$ of perimeter seal width (W) b) $X \leq 3.0\text{mm}$ c) Z disregard d) X and Y not touch crossover dot	
Corner Chip	Corner chip within seal area	Accept if:- a) $X \leq 1/3$ of perimeter seal width (W) b) $Y \leq 1/3$ of perimeter seal width (W) c) Z disregard	



Defect Category	Defect Description	Criterion	Drawing Specification
	Corner chip not effecting contact pad / ITO	Accept if:- a) $XY \leq 4\text{mm}^2$ AND b) $Y \leq D$ and $X \leq 2.0\text{mm}$ c) Z disregard	
	Corner chip effecting contact pad / ITO	A) Accept if:- a) $XY \leq 4\text{mm}^2$ AND b) $Y \leq D$ and $X \leq 2.0\text{mm}$ B) Accept if:- a) $X1 \leq 2.0\text{mm}$ b) $Y1 \leq 0.5\text{mm}$ Z disregard	
Glass flare	A thin layer of glass flare at contact area	Accept if:- a) Flare thickness $\leq \frac{1}{4}W$ when $W \leq 3\text{mm}$ b) Flare thickness $\leq 1\text{mm}$ when $W > 3\text{mm}$ W: Contact ledge width	
Glass burr	A rough edge(s) left along the scribing edge (i.e. along the edges of display)	Reject – if the burr cause undersize or oversize of the LCD	Refer to LCD Physical Dimension Drawing
Rainbow	Colored ring in sharp blotches observed	Reject – if 3 or more colored rings in sharp blotches of color are observed. (Limit samples should be used when applicable)	

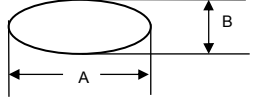


Defect Category	Defect Description	Criterion	Drawing Specification
Discoloration		Reject - if the discolorations enter the active viewing area of LCD. Color of the LCD shall follow product specification as specified in the manufacturing specification	
Air Void	LC does not fulfill the display	Reject	
Fill end contamination	Discoloration at end seal area	Reject if discoloration exceeded the baffle (for display with baffle) or viewing area (for display without baffle)	

12.2.3 POLARIZER DEFECTS

Defect Category	Defect Description	Criterion	Drawing Specification
Polarizer defect	Polarizer coverage	1- Polarizer should cover effective viewing area of display. 2- It is acceptable if perimeter seal bolder at all sides could be seen. 3- It is acceptable if polarizer attaching position meeting the tolerance mentioned in the drawing. 4- It is reject able if polarizer edge jagged and not even	Refer to LCD Physical Dimension Drawing
	Polarizer Peeling / delamination	1- Reject if any edge or corner of the polarizer is lifted up or not adheres to the glass	
	Polarizer Scratches	1- Any scratch should be acceptable if it is not visible from viewing distance at head of position 2- Polarizer scratch in viewing area is reject able if it is visible from the specified viewing distance 3- Defect, which is visible under surface glare, should be disregard	
Defect	Defect Description	Criterion	Drawing Specification

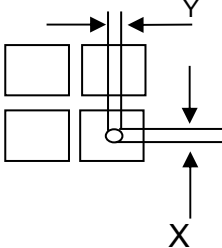
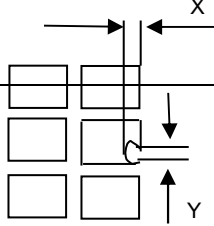
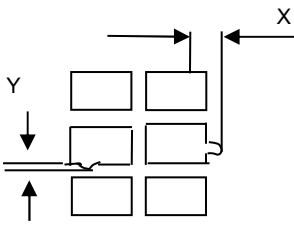


Category																							
	Polarizer damage	1-Stain mark or depression in front polarizer surface should be acceptable if it is not visible from viewing distance at head on position. 2-Defect, which is visible under surface glare, should be disregard																					
	Polarizer bubble / Foreign material	<table border="1"> <thead> <tr> <th rowspan="2">Zone / Dimension</th><th colspan="3">Acceptable No.</th></tr> <tr> <th>A</th><th>B</th><th>C</th></tr> </thead> <tbody> <tr> <td>$D \leq 0.30\text{mm}$</td><td>NC</td><td>NC</td><td rowspan="4">NC if the Polarizer not lifted up/ peel off</td></tr> <tr> <td>$D \leq 0.50\text{mm}$</td><td>2</td><td>NC</td></tr> <tr> <td>$0.50 < D \leq 0.60\text{mm}$</td><td>1</td><td>2</td></tr> <tr> <td>$D > 0.60\text{mm}$</td><td>0</td><td>0</td></tr> </tbody> </table> NC: No count D: Mean Diameter of Defect 3 are the totally permissible numbers of bubble	Zone / Dimension	Acceptable No.			A	B	C	$D \leq 0.30\text{mm}$	NC	NC	NC if the Polarizer not lifted up/ peel off	$D \leq 0.50\text{mm}$	2	NC	$0.50 < D \leq 0.60\text{mm}$	1	2	$D > 0.60\text{mm}$	0	0	 $D = (A + B)/2$
Zone / Dimension	Acceptable No.																						
	A	B	C																				
$D \leq 0.30\text{mm}$	NC	NC	NC if the Polarizer not lifted up/ peel off																				
$D \leq 0.50\text{mm}$	2	NC																					
$0.50 < D \leq 0.60\text{mm}$	1	2																					
$D > 0.60\text{mm}$	0	0																					

12.2.4 ELECTRICAL TEST DEFECTS

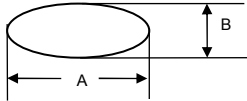
Defect Category	Defect Description	Criterion	Drawing Specification
Missing common	Part of the pattern does not light up	Reject	
Missing segment	One or few segment does not light up	Reject	
Common-common short	Common and common connected	Reject	
Segment-segment short	Segment and segment connected	Reject	
Common – segment short	Common and segment connected	Reject	
Wrong viewing angle	Wrong viewing angle	Reject if display viewing angle not conform to customer requirement	
Metal residue	Extra spot lights up at the border of the segment.	Accept if $\leq 0.20\text{mm}$ (mean diameter)	
Slow response	Response of the display on one side slower than the other side	Reject if it is visible at 30cm distance	
Defect	Defect Description	Criterion	Drawing Specification



Category			
Pin Hole	Pin hole / void at light up segment	Zone / Dimension	Acceptable No.
		Located inside single pixel/dot:- $(X + Y)/2 \leq 0.20\text{mm}$	- 1 per pixel/dot - 3 per display (Active Area)
		Laid over the plural pixel/dots: $(X + Y)/2 \leq 0.20\text{mm}$	- 1 per pixel/dot - 3 per display (Active Area)
		<i>($\frac{3}{4}$ or larger part of dot area has to be effective for display)</i>	
			
Deformed display dot	Lacked deformation	Accept if: i) $X \leq 0.15$ and ii) $Y \leq 0.15$	
	Added deformation	Accept if: i) $X < 0.02$ and ii) $Y < 0.02$	
Reverse twist/ tilt	Segment are darker or clearer than other area of the same segment	Reject	
Misalignment	Segment fatter or smaller or extra segment	Reject if $> 10\%$ of designed segment width and visible at 30cm distance	
Segment Smearing	Light up segment smear	Reject	
Dim segment	Display shows poor contrast at pre set voltage	Reject	



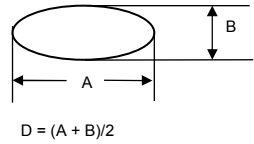
12.2.5 BLACK SPOT, WHITE SPOT AND FOREIGN MATERIAL (SOLID FIGURE)

Defect Category	Defect Description	Criterion				Drawing Specification	
Black Spot, White Spot and Foreign Material	Black Spot, White Spot and Foreign Material	Zone / Dimension		Acceptable No.			 D = (A + B)/2
				A	B	C	
		D ≤ 0.10mm		NC	NC	NC	
		0.10<D ≤ 0.15mm		3	3	NC	
		0.15 < D ≤ 0.25mm		1	2	NC	
		0.25 < D ≤ 0.35mm		1	1	NC	
		D > 0.35 mm		0	0	NC	
		NC: No count					
		D: Mean Diameter of Defect					

NOTE: The 1/3 or larger parts of individual dot has to be lighted on.

The solid figure is that the defect has clear-cut outline at the optimum driving condition in both positive and negative, of which size does not change when the contrast changes.

12.2.6 BLACK SPOT, WHITE SPOT AND FOREIGN MATERIAL (FADED FIGURE)

Defect Category	Defect Description	Criterion				Drawing Specification	
Black Spot, White Spot and Foreign Material	Black Spot, White Spot and Foreign Material	Zone / Dimension		Acceptable No.			
				A	B	C	
		D ≤ 0.60mm	NC	NC	NC		
		0.60<D ≤ 0.70mm	3		NC		
		0.70 < D ≤ 0.80mm	1		NC		
		D > 0.80 mm	0		NC		
		NC: No count					
		D: Mean Diameter of Defect					

NOTE: Faded figure means that the defects has unclear outline at the optimum driving condition in both positive and negative, of which size seems to change when the contrast changes.



12.2.7 LINE SHAPE AND SCRATCHES

Defect Category	Defect Description	Criterion					Drawing Specification
Line shape and scratches	Line shape and scratches						
		Zone /Dimension		Acceptable No.			
		X	Y	A	B	C	
		NC	$\leq$ 0.03mm	NC	NC	NC	
		≤ 2 mm	$\leq$ 0.05mm	1	1	NC	
		≤ 1 mm	$\leq$ 0.10mm	1	2	NC	
		NC	$\geq$ 0.10mm	Due to (1) round defect			

NOTE: Length is X and Width is Y.

REMARK:

i) Total amount of spot defects including round and linear – A total of 5 permissible numbers of defects in Zone A & B including above (14.2.5), (14.2.6), (14.2.7). Regardless of number of defects, the minimum distance between individual defects have to be 5mm or larger.

ii) All the other items of inspection that are not included herein must be determined by the “Limit Standard” sample, which were occasionally set up with the mutual consent of both parties. In every case of the items set up with the Limit Standard, the Limit Standard always takes precedence over the other means of definition.



13. Precaution for using LCM

1. Liquid Crystal Display (LCD)

LCD is made up of glass, organic sealant, organic fluid and polymer based polarizers. The following precautions should be taken when handling.

- a) Keep the temperature within the range of use and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel off or bubble.
- b) Do not contact the exposed polarizer with anything harder than HB pencil lead. To clean dust off the display surface, wipe gently with cotton, chamois or other soft material soaked in petroleum benzene.
- c) Wipe off saliva or water drops immediately. Contact with water over a long period of time may cause polarizer deformation or colour fading, while an active LCD with water condensation on its surface will cause corrosion of ITO electrodes.
- d) Glass can be easily chipped or cracked from rough handling, especially at corners and edges.
- e) Do not drive LCD with DC voltage.

2. Liquid Crystal Display Modules.

2.1 Mechanical Considerations

LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modification. The following should be noted.

- a) Do not tamper in any way with the tabs on the metal frame.
- b) Do not modify the PCB by drilling extra holes, changing its outline, moving its component or modifying its pattern.
- c) Do not touch the elastomer connector, especially at conductor area.
- d) When mounting a LCM make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
- e) Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.

2.2 Static Electricity

LCM contains CMOS LSI's and the same precaution for such devices should apply, namely

- a) The operator should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.
- b) The modules should be kept in antistatic bags or other antistatic containers.
- c) Only properly grounded soldering irons should be used.
- d) If an electric screwdriver is used, it should be well grounded and shielded from spark commutator.
- e) The normal static prevention measures should be observed for work clothes and working benches, the latter conductive (rubber) mat is recommended.
- f) Since dry air is inductive to statics, a relative humidity of 50-60% is recommended.

2.3 Operation

- a) Viewing angle varies with the change of liquid crystal driving voltage (VLCD). VLCD has to be adjusted to show the best contrast.
- b) It is a necessary condition to drive LCD's within the specified voltage limit since at the higher voltage limit this can result in shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- c) Response times will be delayed at lower temperature than the operating temperature range and on the other hand, at higher temperature LCD's show darker color in them. However those phenomena do not mean a malfunction or out of order with the LCD's which will recover in the specified operating temperature.
- d) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.



- e) A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%RH or less is required.
- f) Input logic voltage before apply analog high voltage such as LCD driving voltage when power on. Remove analog high voltage before logic voltage when power off the module. Input each signal after the positive/negative voltage becomes stable.
- g) It is advisable to keep the temperature within the specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

2.4 Soldering

- a) Solder only to the I/O terminals.
- b) Use only soldering irons with proper grounding and no leakage.
- c) Soldering temperature: 280 °C
- d) Soldering time: 3 to 4 sec
- e) Use eutectic solder with resin flux fill.
- f) If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed afterwards.

2.5 Storage

If any fluid leaks out of the damage glass cell, wash off any human part that comes into contact with soap and water. Never swallow the fluid. The toxicity is extremely low but caution should be exercised at all time.

3. Limited Warranty

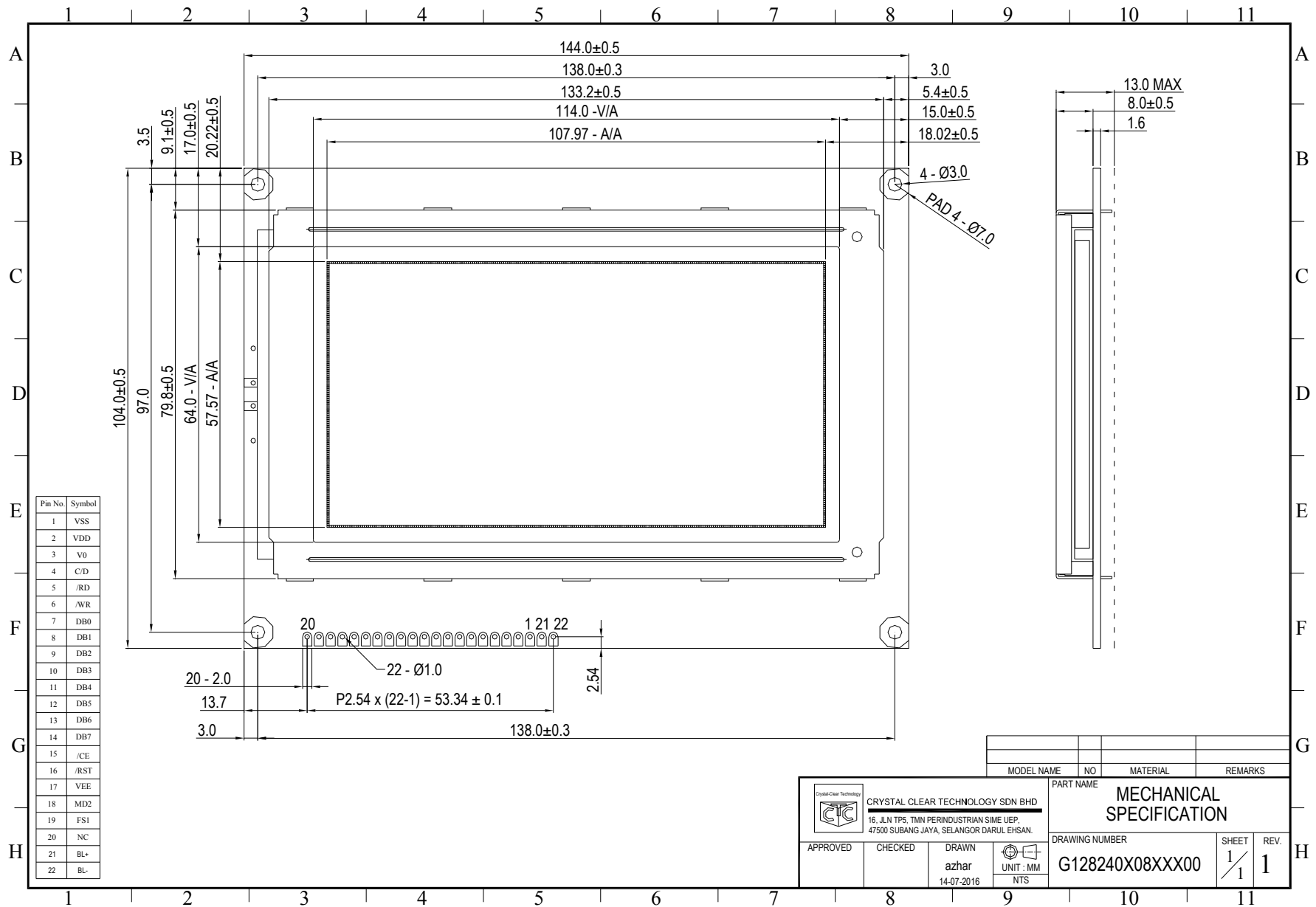
Unless otherwise agreed between Crystal Clear Technology and customer, Crystal Clear Technology will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with Crystal Clear Technology acceptance standards, for a period of one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of Crystal Clear Technology is limited to repair and/or replacement on the terms set forth above. Crystal Clear Technology will not responsible for any subsequent or consequential events.

4. Return LCM under Warranty

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are:

- i. Broken LCD glass
- ii. PCB eyelet's damaged or modified
- iii. PCB conductors damaged
- iv. Circuit modified in any way, including addition of components.
- v. PCB tampered with by grinding, engraving or painting varnish.
- vi. Soldering to, or modifying the bezel in any manner.

Module repairs will be invoiced to customer upon mutual agreement. Modules must be returned with sufficient description of failure or defects. Any connectors or cable installed by customer must be removed completely without damaging the PCB eyelet's, conductors and terminals.





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