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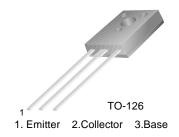
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FJE3303

High Voltage Fast-Switching NPN Power Transistor

- · High Voltage Capability
- High Switching Speed
- Suitable for Electronic Ballast and Switching Regulator



Absolute Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	700	V
V _{CEO}	Collector-Emitter Voltage	400	V
V _{EBO}	Emitter-Base Voltage	9	V
I _C	Collector Current (DC)	1.5	A
I _{CP}	Collector Current (Pulse) *	3	A
I _B	Base Current (DC)	0.75	A
I _{BP}	Base Current (Pulse) *	1.5	A
P _C	Collector Dissipation (T _C = 25°C)	20	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-65 ~ 150	°C

^{*} Pulse Test: Pulse Width = 5ms, Duty Cycle \leq 10%

Electrical Characteristics $T_C = 25$ °C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Тур.	Max	Units
BV _{CBO}	Collector-Base Breakdwon Voltage	$I_C = 500\mu A, I_E = 0$	700			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 5mA, I_B = 0$	400			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 500 \mu A, I_C = 0$	9			V
I _{CBO}	Collector Cut-off Current	V _{CB} = 700V, I _E = 0			10	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 9V, I_{C} = 0$			10	μΑ
h _{FE1} h _{FE2}	DC Current Gain *	$V_{CE} = 2V, I_{C} = 0.5A$ $V_{CE} = 2V, I_{C} = 1.0A$	8 5		21	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	$I_C = 0.5A, I_B = 0.1A$ $I_C = 1.0A, I_B = 0.25A$ $I_C = 1.5A, I_B = 0.5A$			0.5 1.0 3.0	V V V
V _{BE(sat)}	Base-Emitter Saturation Voltage	$I_C = 0.5A, I_B = 0.1A$ $I_C = 1.0A, I_B = 0.25A$			1.0 1.2	V V
f _T	Current Gain Bandwidth Product	$V_{CE} = 10V, I_{C} = 0.1A$	4			MHz
C _{ob}	Output Capacitance	V _{CB} = 10V, f = 0.1MHz		21		pF
t _{ON}	Turn On Time	V _{CC} = 125V, I _C = 1A			1.1	μs
t _{STG}	Storge Time	$I_{B1} = 0.2A, I_{B2} = -0.2A$ $-R_{L} = 125\Omega$			4.0	μs
t _F	Fall Time				0.7	μs

^{*} Pulse Test: PW $\leq 300 \mu s, \, \text{Duty Cycle} \leq 2\%$

h_{FE} Classification

Classification	H1	H2
h _{FE1}	8 ~ 16	14 ~ 21

Typical Performance Characteristics

Figure 1. Static Characteristic

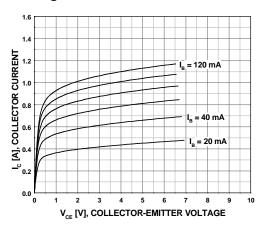


Figure 2. DC Current Gain

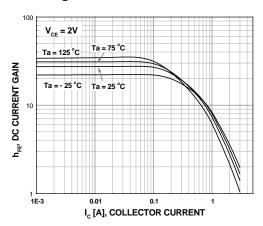


Figure 3. Collector-Emitter Saturation Voltage

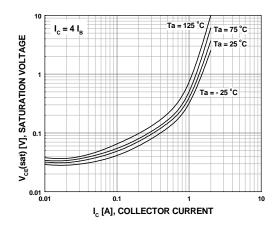


Figure 4. Base-Emitter Saturation Voltage

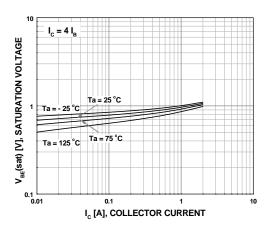


Figure 5. Resistive Load Switching Time

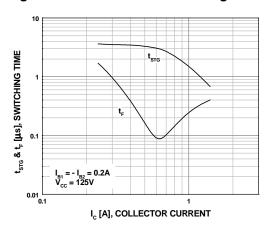
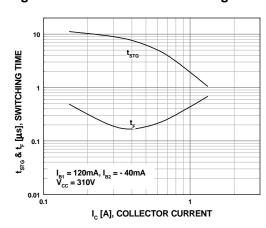


Figure 6. Resistive Load Switching Time



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Typical Performance Characteristics (Continued)

Figure 7. Forward Biased Safe Operating Area Figure 8. Re

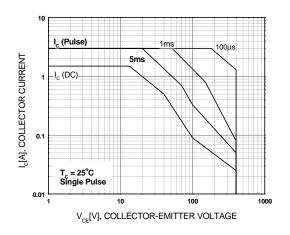


Figure 8. Reverse Biased Safe Operating Area

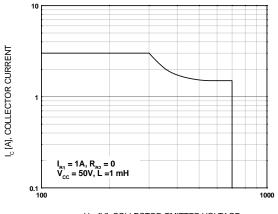
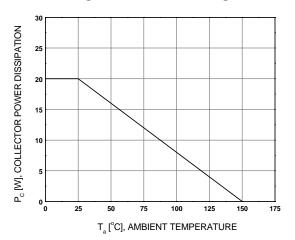
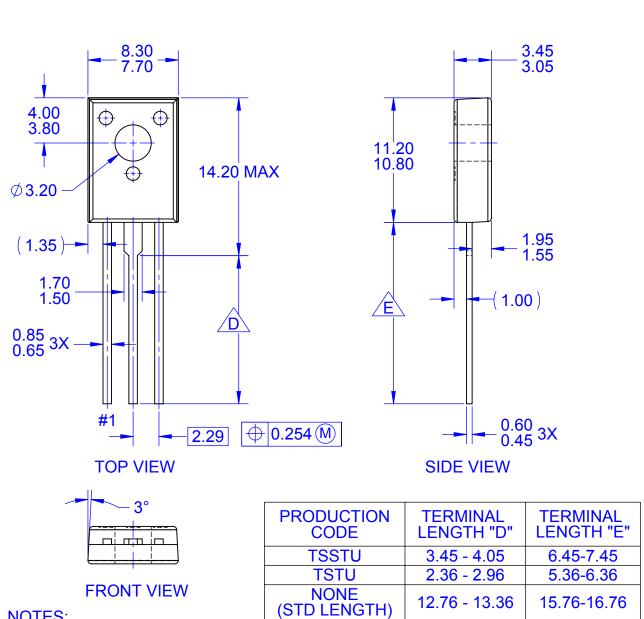


Figure 9. Power Derating



 $V_{CE}[V]$, COLLECTOR-EMITTER VOLTAGE



- **NOTES:**
- A. NO INDUSTRY STANDARD APPLIES TO THIS **PACKAGE**
- B. ALL DIMENSIONS ARE IN MILLIMETERS
- C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR PROTRUSIONS





FOR TERMINAL LENGTH "E", REFER TO TABLE

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