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DC918C-G - LTC2204 Evaluation Kit (Fin= 1-70MHz)

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COMPANY

SIMULATE

FEATURES

 To be used in conjunction with the DC718B - QuickDAACS USB Controller for QuickEval-II Evaluation Kits

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• Disclaimer: Linear Technology cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Linear Technology product. No circuit patent licenses are implied. Linear Technology reserves the right to change the circuitry and specifications without notice at any time.

TYPICAL APPLICATION

FEATURES DESCRIPTION



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DESCRIPTION

Demonstration circuit 918 supports members of a family of 16/14-Bit 40Msps to 105Msps ADCs. Each assembly features one of the following devices: LTC2207, LTC2207-14, LTC2206, LTC2206-14, LTC2205, LTC2205-14, or LTC2204 high speed, high dynamic range ADCs.

Other members of this family include the LTC2208 16-Bit 130Msps ADC with LVDS outputs, as well as the LTC2203 and LTC2202 which are 16-Bit 25Msps and 10Msps single-ended clock versions. These 7x7mm QFN devices are supported by Demonstration Circuit 919 (for single-ended clock input).

Several versions of the 918A demo board supporting the LTC2207 16-Bit and LTC2207-14 14-Bit series of A/D converters are listed in Table 1. Depending on the required resolu-tion, sample rate and input frequency, the DC918 is supplied with the appropriate ADC and with an optimized input circuit. The cir-cuitry on the analog inputs is optimized for analog input frequencies below 70MHz or from 70MHz to 140MHz. For higher input frequencies, contact the factory for support.

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ORDER INFO

- Part numbers ending in PBF are lead free. Please contact LTC marketing for information on lead based finish parts.
- Part numbers containing TR or TRM are shipped in tape and reel or 500 unit mini tape and reel, respectively
- Please refer to our general ordering information or the product datasheet for more details

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APPLICATIONS

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SIMULATE

The QuickEval-II System is a USB-based product demonstration and data acquisition system for use with Linear Technology's high performance ADC's. The system consists of a USB Data Acquisition Controller board that connects to individual daughter cards for specific Linear Technology ADCs. The softw are collects and analyzes data from the ADC in both the time and frequency domains and displays relevant parameters for the device being evaluated. The softw are also provides an automatic update feature to keep the softw are current with the latest set of device drivers.

Click Here to Dow nload Softw are for the QuickEval II System

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