

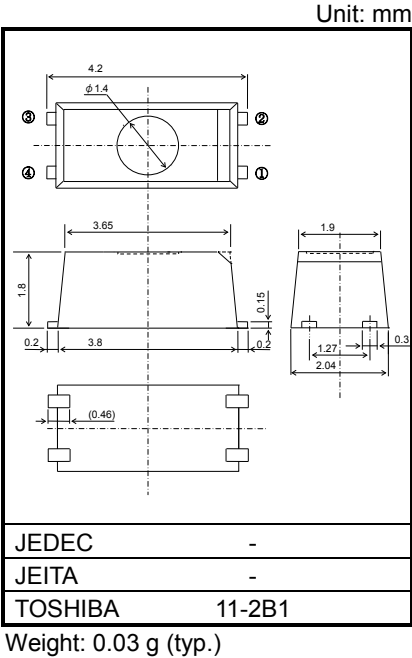
TLP3214

Measuring Instruments
Logic IC Testers / Memory Testers
Board Testers / Scanners

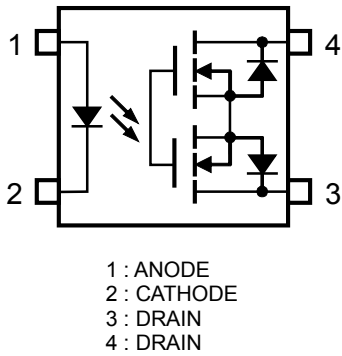
The TOSHIBA TLP3214 is an ultra-small photorelay suitable for surface-mount assembly. The TLP3214 consists of a GaAs infrared-emitting diode optically coupled to a photo-MOSFET and is housed in a 4-pin package. Its features include low Off-state current and low output pin capacitance, enabling it to be used for high-frequency measuring instrument applications.

Features

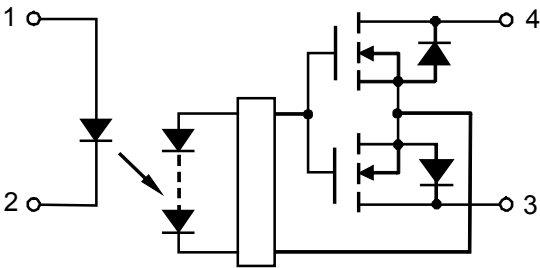
- 4-pin SSOP (SSOP4): 1.8 mm high, 1.27 mm pitch
- 1-Form-A
- Peak Off-State Voltage: 40 V (min)
- Trigger LED Current: 4 mA (max)
- On-State Current: 250 mA (max)
- On-State Resistance: 3 Ω (max), 2 Ω (typ.)
- Output Capacitance: 7 pF (max), 5 pF (typ.)
- Isolation Voltage: 1500 Vrms (min)
- UL approved: UL1577, File No.E67349



Pin Configuration (Top View)



Schematic



Start of commercial production
2002-09

Absolute Maximum Ratings (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
LED	Forward Current	I_F	50	mA
	Forward Current Derating (Ta ≥ 25°C)	$\Delta I_F / ^\circ\text{C}$	-0.5	mA/°C
	Reverse Voltage	V_R	5	V
	Diode Power Dissipation	P_D	50	mW
	Diode Power Dissipation Derating (Ta ≥ 25°C)	$\Delta P_D / ^\circ\text{C}$	-0.5	mW/°C
	Junction Temperature	T_j	125	°C
DETECTOR	Off-State Output Terminal Voltage	V_{OFF}	40	V
	On-State Current	I_{ON}	250	mA
	On-State Current Derating (Ta ≥ 25°C)	$\Delta I_{ON} / ^\circ\text{C}$	-2.5	mA/°C
	Output Power Dissipation	P_O	188	mW
	Output Power Dissipation Derating (Ta ≥ 25°C)	$\Delta P_O / ^\circ\text{C}$	-1.88	mW / °C
	Junction Temperature	T_j	125	°C
Storage Temperature Range		T_{stg}	-40 to 125	°C
Operating Temperature Range		T_{opr}	-20 to 85	°C
Lead Soldering Temperature (10 s)		T_{sol}	260	°C
Isolation Voltage (AC, 60 s, R.H. ≤ 60%) (Note 1)		BVS	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two-terminal device. Pins 1 and 2 shorted together, and pins 3 and 4 shorted together.

Caution

This device is sensitive to electrostatic discharge. When using this device, please ensure that all tools and equipment are earthed.

Recommended Operating Conditions

CHARACTERISTIC	SYMBOL	MIN	TYP.	MAX	UNIT
Supply Voltage	V_{DD}	—	—	32	V
Forward Current	I_F	—	—	30	mA
On-State Current	I_{ON}	—	—	250	mA
Operating Temperature	T_{opr}	-20	—	65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
LED	Forward Voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
	Reverse Current	I _R	V _R = 5 V	—	—	10	μA
	Capacitance	C _T	V = 0 V, f = 1 MHz	—	15	—	pF
DETECTOR	Off-State Current	I _{OFF}	V _{OFF} = 30 V, Ta = 50°C	—	—	1000	pA
	Capacitance	C _{OFF}	V = 0 V, f = 100 MHz, t < 1 s	—	5	7	pF

Coupled Electrical Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Trigger LED Current	I _{FT}	I _{ON} = 100 mA	—	—	4	mA
Return LED Current	I _{FC}	I _{OFF} = 10 μA	0.2	0.75	—	mA
On-State Resistance	R _{ON}	I _{ON} = 250 mA, I _F = 5 mA, t < 1 s	—	2	3	Ω

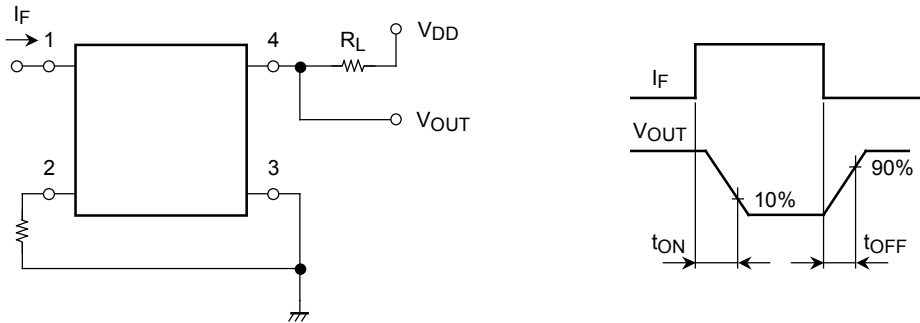
Isolation Characteristics (Ta = 25°C)

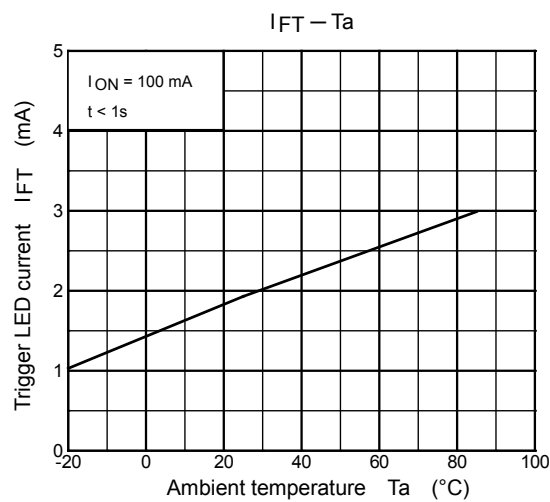
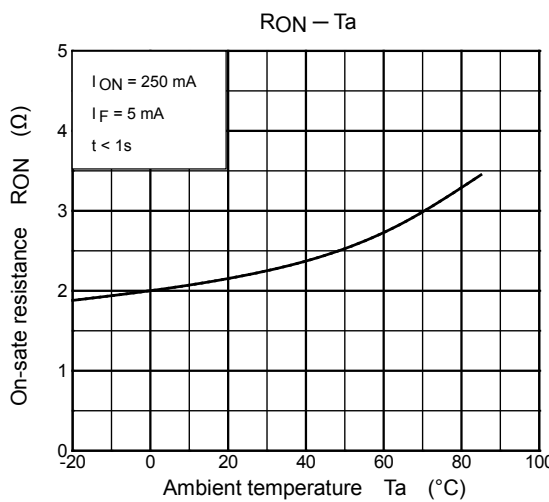
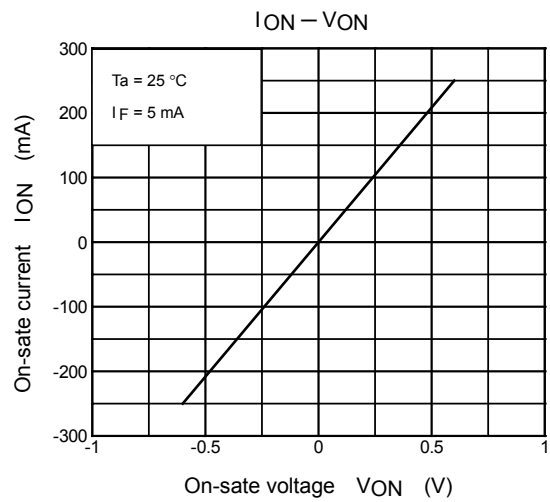
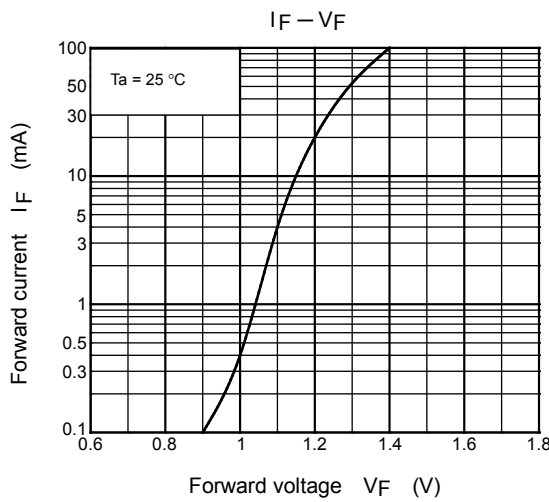
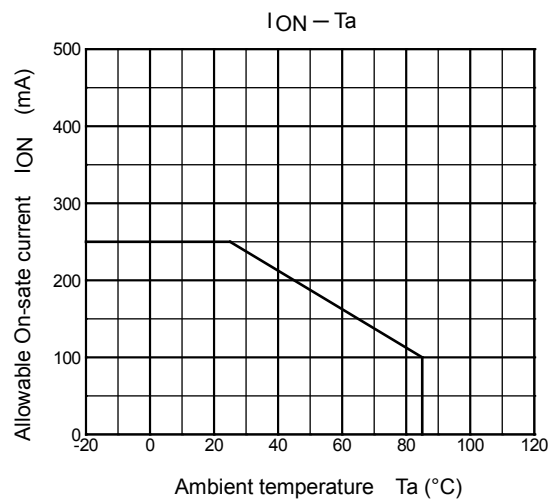
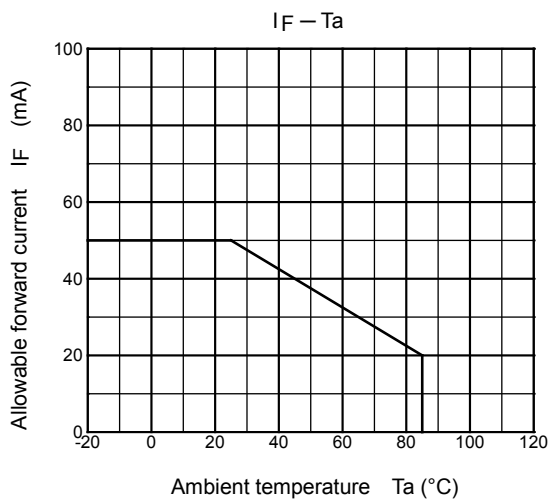
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Capacitance Input to Output	C _S	V _S = 0 V, f = 1 MHz	—	0.3	—	pF
Isolation Resistance	R _S	V _S = 500 V, R.H. ≤ 60%	5 × 10 ¹⁰	10 ¹⁴	—	Ω
Isolation Voltage	BV _S	AC, 60 s	1500	—	—	V _{rms}
		AC, 1 s (in oil)	—	3000	—	
		DC, 60 s (in oil)	—	3000	—	V _{dc}

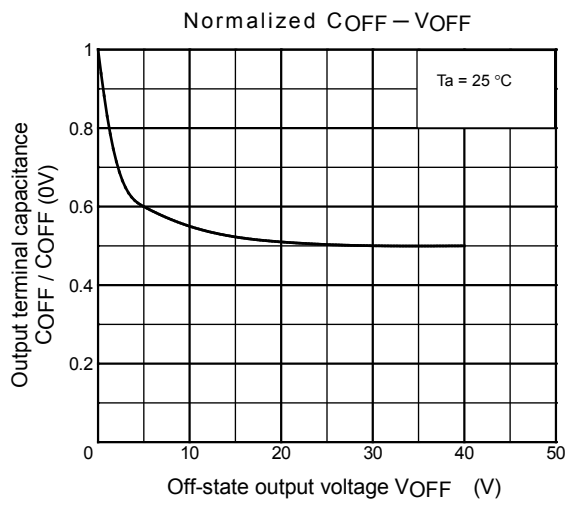
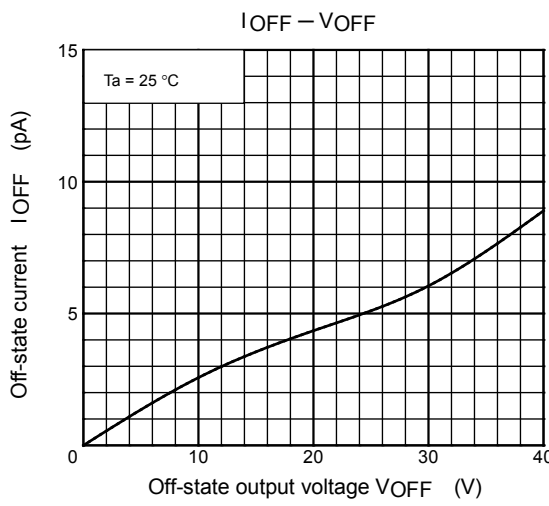
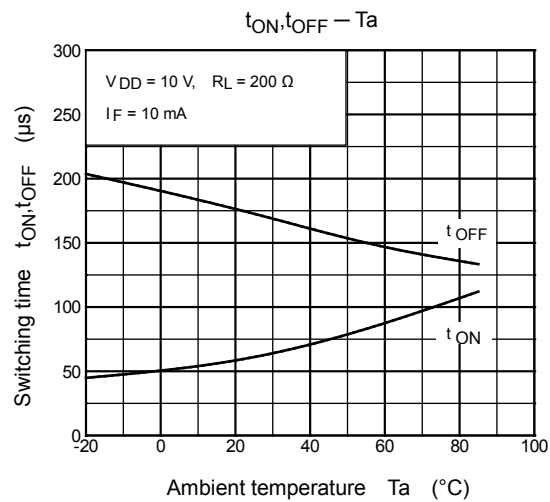
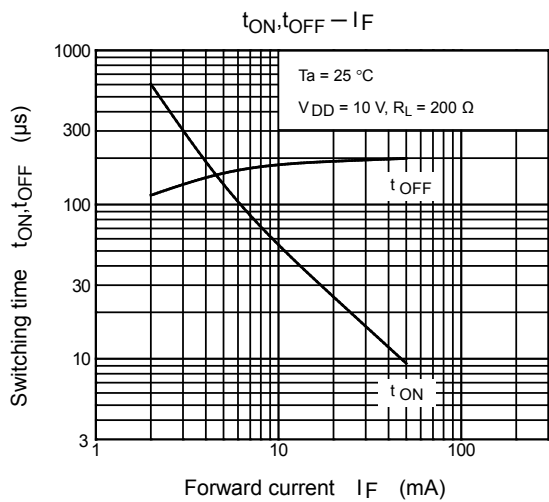
Switching Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Turn-on Time	t _{ON}	R _L = 200 Ω (Note 2) V _{DD} = 10 V, I _F = 5 mA	—	120	500	μs
Turn-off Time	t _{OFF}		—	140	500	

Note 2: SWITCHING TIME TEST CIRCUIT



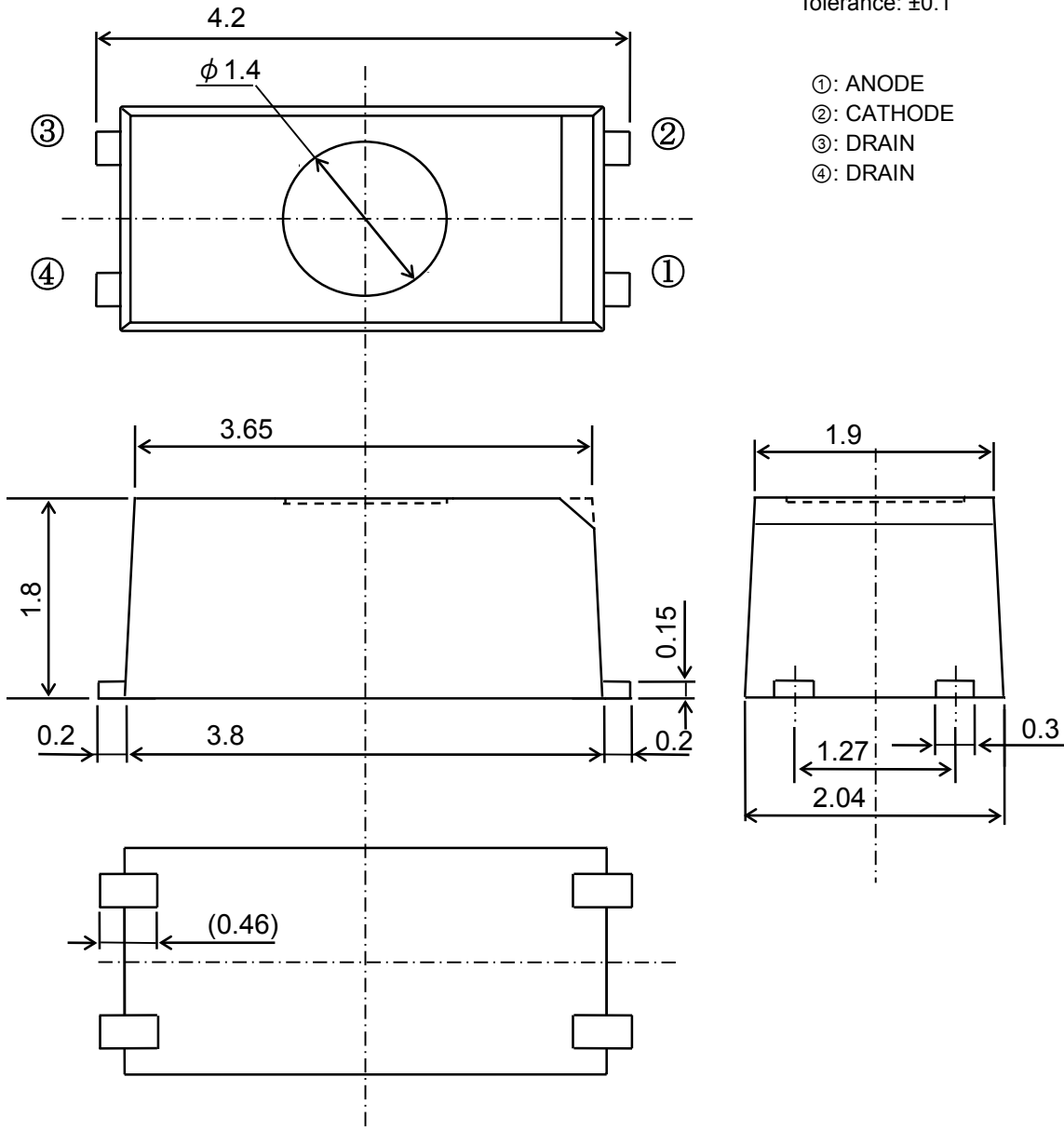




Package Dimensions

Unit: mm
Tolerance: ±0.1

- ①: ANODE
- ②: CATHODE
- ③: DRAIN
- ④: DRAIN



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